

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
1B21	TRANSMITTER, LOCALLY POWERED, INTERNAL LOOP SUPPLY, 4 to 20 mA OUTPUT, CALIBRATED 2/10V RANGES, ACCURACY=0.05%
1B22	TRANSMITTER, LOOP POWERED, ISOLATED 1500VRMS, PIN PROGRAMMABLE ,0/4 to 20 mA, ACCURACY=0.05%
1B31	STRAIN GAUGE, w/BRIDGE EXCITATION, CMRR=100dB, LINEARITY=0.005%, TC <sub>Vos</sub> =2uV/C, GAIN ERROR=3%
1B32	STRAIN GAUGE, w/BRIDGE EXCITATION, CMRR=100dB, LINEARITY=0.005%, TC <sub>Vos</sub> =0.2uV/C, GAIN ERROR=0.1%
1B41	RTD, ISOLATED, 5ohms to 5Kohms FS, CMRR=160dB, LINEARITY= 0.025%, GAIN ERROR=5% FS
1B51	ISOLATED THERMOCOUPLE or mV SIGNAL CONDITIONER, 1500VRMS, CMRR=160dB, LINEARITY=0.05%
292	2 PORT, ±2000V, CLOCK POWERED, Requires AD246 Clock Oscillator, See AD204, AD104 for new designs
AC1226	COLD END COMPENSATOR, FOR THERMOCOUPLES
AC2626	AD590 MOUNTED IN A STAINLESS STEEL PROBE
AD102	2 PORT, CMV=±500V, +15V POWERED, LOW COST
AD10242	12 BIT, 41MSPS, DUAL, 2 AD9042 w/ INPUT SIGNAL CONDITIONING, +/-5V Rails
AD104	2 PORT, CMV=±500V, CLOCK POWERED, Requires AD246 Clock Oscillator, LOW COST
AD1315	ACTIVE LOAD, 1.5nsec prop delay, linearity=0.12% FS, VOLTAGE PROGRAMMABLE I <sub>sink</sub> =50mA
AD1317	WINDOW COMPARATOR, ULTRA HIGH SPEED 2.5nsec prop delay, 400psec dispersion, WITH LATCH
AD1324	PIN DRIVER, 200MHz, -2V to +7V, 100mA OUTPUT, SERIES OUTPUT SWITCH, 1.6nsec Inhibit Mode
AD1376	16 BIT, 10 usec, Hybrid, See AD676/677 for new designs
AD1380	16 BIT, 50KSPS, Hybrid, SEE AD676/677
AD1382	16 BIT, 500MSPS, Hybrid, WITH ZERO AUTO CALIBRATION
AD1385	16 BIT, 500MSPS, Hybrid, WITH ZERO & GAIN AUTO CALIBRATION
AD1403	+2.5V, Vos=25mV, TC <sub>Vos</sub> =40ppm, See AD680 or AD780 for new designs
AD1403A	+2.5V, Vos=5>25mV, TC <sub>Vos</sub> =25ppm, See AD680 or AD780 for new designs
AD14060	QUAD ADSP21060 MULTI-CHIP MODULE, 480 MFLOPS Peak, 320 MFLOPS Sustained, +5V

[Click on a part to see Datasheet.](#)

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PART	DESCRIPTION
AD14060L	QUAD ADSP21060 MULTI-CHIP MODULE, 480 MFLOPS Peak, 320 MFLOPS Sustained, +3V
AD14160	QUAD ADSP21060 MULTI-CHIP MODULE, 480 MFLOPS Peak, 320 MFLOPS Sustained, 452 Lead Ceramic ball grid array
AD14160L	QUAD ADSP21060 MULTI-CHIP MODULE, 480 MFLOPS Peak, 320 MFLOPS Sustained, 452 Lead Ceramic ball grid array
AD1580	+1.2V, Eos=10mV, TCVos=50>100ppm/C
AD1581	+1.225V, 3 TERMINAL REGULATOR, Iq=50uA, 50 or 100 ppm
AD1582	+2.5V, 1/.1% ACCURACY 50/100 ppm, 3 TERMINAL , Iq=50uA, Band GAP, Source/Sink 5mA
AD1583	+3.0V, 1/.1% ACCURACY 50/100 ppm, 3 TERMINAL , Iq=50uA, Band GAP, Source/Sink 5mA
AD1584	+4.096V, 1/.1% ACCURACY 50/100 ppm, 3 TERMINAL , Iq=50uA, Band GAP, Source/Sink 5mA
AD1585	+5.0V, 1/.1% ACCURACY 50/100 ppm, 3 TERMINAL , Iq=50uA, Band GAP, Source/Sink 5mA
AD1586	+2.5V, 1/.1% ACCURACY 50/100 ppm, 2 TERMINAL , Iq=50uA, Band Gap
AD1587	+3.0V, 1/.1% ACCURACY 50/100 ppm, 2 TERMINAL , Iq=50uA, Band Gap
AD1588	+4.096V, 1/.1% ACCURACY 50/100 ppm, 2 TERMINAL , Iq=50uA, Band Gap
AD1589	+5.0V, 1/.1% ACCURACY 50/100 ppm, 2 TERMINAL , Iq=50uA, Band Gap
AD1672	12 BIT, 2.2 MSPS, +5V RAIL, @ .5MHz SINAD=63dB, SNR=66dB, THD=-64dB, SFDR=-65dB, 200mW
AD1674	12 BIT, 100KSPS, 8/16 BIT I/O, ALL INPUT RANGES, Industry STD Sampling A/D
AD1816A	16 BIT SINGLE CHIP, SOUND PORT CONTROLLER, ISA PLUG & PLAY, SOUND BLASTER COMPATIBLE, AUDIO SUB-SYSTEM, + MORE FEATURES
AD1817A	16 BIT SOUND PORT CONTROLLER, ISA AUDIO SUB SYSTEM SOUND BLASTER & PC97 COMPATIBALE, MPU-401 MIDI PORT, Analog/Digital HW volume control, 1KHZ>48KHZ w/1HZ RESOLUTION
AD1818	PCI DIGITAL CONTROLLER FOR AUDIO & COMMUNICATIONS, PCI BUS INTERFACE, WDM MIXER/SAMPLE RATE CONVERTER, CSPO1 DSP, AC 97 SERIAL PORT
AD1819	16-BIT, AC-97 SOUND PORT CODEC, 5.5KHZ>48KHZ SMAPLE RATE, 1HZ RESOLUTION, 4 LINE LEVEL STEREO, 2 MONO INPUTS, PHAT STEREO 3D STEREO ENHANCEMENT, DSP SERIAL PORT MODE, SUPPORTS MODEM SAMPLE RATES

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD1819A	16-BIT, AC-97 SOUND PORT CODEC, 5.5KHZ>48KHZ SMAPLE RATE, 1HZ RESOLUTION, 4 LINE LEVEL STEREO, 2 MONO INPUTS, PHAT STEREO 3D STEREO ENHANCEMENT, DSP SERIAL PORT MODE, SUPPORTS MODEM SAMPLE RATES, PART OF CYRIX 5520 CHIP SET
AD1820	16 BIT SINGLE CHIP AUDIO /FAX/MODEM SUB-SYSTEM, SOUND BLASTER COMPATIBLE
AD1821	16 BIT SINGLE CHIP AUDIO /FAX/MODEM SUB-SYSTEM, PHAT STEREO, SOUND BLASTER & PC-97 LOGO COMPATIBLE
AD1843	16 BIT, 2 CH A/D, 4 CH D/A, DOES SPEECH, AUDIO, FAX/DATA MODEM ALL ON ONE CHIP
AD1845	16 BIT STEREO CODEC, WITH FIFO, DUAL DMA COUNTERS, EXTENDED SAMPLE RATES, Parallel, variable sample rate, 4KHZ TO 50KHZ, PINS w/AD1848
AD1847	16 BIT STEREO CODEC, 4 STEREO PAIR INPUTS, PRGM GAIN & ATTEN, SAMPLE RATE=5.5 to 48KHzZ, +5V Rail
AD1849K	16 BIT STEREO CODEC, IMPROVED AD1849J DYNAMIC RANGE and THD +N, EXT CLOCK OPTION
AD1851	SINGLE, 16 BIT, 16xFS, THD= 0.004%, SNR=107dB, +/-5V @13mA
AD1855	DUAL, 16/18/20/24 BIT STEREO DAC, +5V RAIL, 96KHZ, Oversampling interpolation filter, 113 to 110dB Dynamic Range, 128x INTERPOLATION FILTER, MULTI-BIT, I2S, DSP, RIGHT or LEFT JUSTIFIED I/O
AD1857	DUAL, 16 BIT STEREO DAC, +5V RAIL, Fclk=256/384*Fsam, Oversampling interpolation filter, 96dB Dynamic Range, 128x INTERPOLATION FILTER, SIGMA DELTA w/Digital Dither, I2S or LEFT JUSTIFIED I/O
AD1858	DUAL, 16 BIT STEREO DAC, +5V RAIL, Fclk=256/384*Fsam, Oversampling interpolation filter, 96dB Dynamic Range, 128x INTERPOLATION FILTER, SIGMA DELTA w/Digital Dither, RIGHT JUSTIFIED or DSP I/O
AD1859	DUAL, 16 or 18 BIT STEREO DAC, +5V RAIL, VARIABLE RATE OVERSAMPLING INTERPOLATION FILTER, SIGMA DELTA w/ Triangular PDF Dither, I2S/RIGHT/LEFT & DSP Serial I/O
AD1861	SINGLE, 18 BIT, 16xFS, THD= 0.008%, SNR=107dB, +/-5V @15mA
AD1862	SINGLE, 20 BIT, 16xFS, THD= 0.0016%, SNR=113dB, +/-5>12V @15mA
AD1865	DUAL, 18 BIT, 8xFS, THD= 0.006%, SNR=107dB, Channel Seperation=110dB, +5/-V @26mA
AD1866	DUAL, 16 BIT, 8xFS, THD= 0.01%, SNR=95dB, Channel Seperation=108dB, +5V @3mA
AD1868	DUAL, 18 BIT, 8xFS, THD= 0.01%, SNR=95dB, Channel Seperation=108dB, +5V @14mA
AD1877	DUAL A/D, 16 BIT, 256or 386xFs, +5V @80mA, THD=-98dB, SNR=90dB, Serial I/O, 92dB SNR
AD1879	DUAL A/D, 18 BIT, 64xFs, +/-5V @13mA, THD=-98dB, SNR=103dB, MODULATORS

[Click on a part to see Datasheet.](#)

# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD1890	STEREO ASYNCHRONOUS SAMPLE RATE CONVERTERS, 4 to 20 BITS, +5V @40mA, Dynamic Range=120dB, THD=-94dB, w/Long or Short Group Delays
AD1891	STEREO ASYNCHRONOUS SAMPLE RATE CONVERTERS, 4 to 16 BITS, +5V @40mA, Dynamic Range=96dB, THD=-96dB
AD1892	INTEGRATED DIGITAL RECEIVER IEC-958 (AES/EBU 7 S/PDIP)/SAMPLE RATE CONVERTER, -96dB THD+N, 120dB Dynamic Range, Sample Rate Conversion 8>48KHz, SPI I/O
AD1893	STEREO ASYNCHRONOUS SAMPLE RATE CONVERTERS, 4 to 16 BITS, +3.3V @20mA, Dynamic Range=96dB, THD=-46dB, 4-Wire Serial Right Justified I/O
AD202	2 PORT, CMV=±2000V, w/ISOLATED OUTPUT POWER, +15V POWERED
AD203	2 PORT, ±2000V, +15V POWER, WIDE TEMP RANGE
AD204	2 PORT, CMV=±2000V, w/ISOLATED OUTPUT POWER, CLOCK POWERED, Requires AD246 Clock Oscillator
AD208	2 PORT, ±2000V, +15V POWER, LOW DRIFT, HIGH ACCURACY
AD20MSP400	NOISE AND ECHO CANCELLATION FOR MOBILE PHONES (GLOSSY) ASSP2176+AD1845 (GLOSSY), H3020-7.5-2/97
AD20MSP410	GSM BASEBAND PROCESSING CHIPSET: ALG (Algorithm signal Processor=ASCIC Variant of ADSP2176 & ADSP2178, AD-PLP01, Physically Layer Processor=ASCIC +H8300H u/CONTROLLER, BBC, Base Band Converter=AD53/009-9 (AD7115)
AD20MSP415	GSM BASEBAND PROCESSING CHIPSET: AD6421+AD6422, COMES WITH LAYER S/WARE, ALSO AVAILABLE OBJECT CODE & LICENSE FOR LAYERS 2&3 OF THE PROCOTOL STACK and USER INTERFACE DEVELOPMENT SYSTEM
AD20MSP910	ADSL CHIPSET, AD6435 modem interface, AD6436 DMT Co-Processor, AD6437 Analog Front End, AD816 Line Driver and Receiver, ADSP2183 Fixed Point DSP
AD210	3 PORT, CMV=±3500V, +15V POWER
AD215	2 PORT, CMV=±1500V, +15V POWER, WIDE BANDWIDTH, 100KHZ , THD=80dB @ 1KHz
AD22001	LAMP (BULB MONITOR) MONITOR, 5 Channel, w/Cold Bulb Test &
AD22050	HIGH SIDE SIGNAL CONDITIONER, TRANSIENT SPIKE & RFI PROTECTION, Av=1 to 160 ADJ. or Fixed Av=20, Vs=+3 or =5V, LARGE CMV RANGE>5x Vcc
AD22055	PIN PROGRAMMABLE, BRIDGE AMPLIFIER, HIGH SIDE SIGNAL CONDITIONER, TRANSIENT SPIKE & RFI PROTECTION, Av=40 to 1000, LARGE CMV RANGE>5x Vcc, NOT OPEN MARKET PART

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD22057	HIGH SIDE SIGNAL CONDITIONER, TRANSIENT SPIKE & RFI PROTECTION, $A_v=1$ to 160 ADJ. or Fixed $A_v=20$ , $V_s=+5V$ , LARGE CMV RANGE $>5 \times V_{cc}$ , NOT OPEN MARKET PART
AD22100	TEMPERATURE SENSOR WITH SIGNAL CONDITIONING, OUTPUT IS RATIOMETRIC TO $V_{cc}$ , -50 to 150C, 22.5mV/C
AD22103	TEMPERATURE SENSOR WITH SIGNAL CONDITIONING, OUTPUT IS RATIOMETRIC TO $V_{cc}$ , 0 to 100C, 28mV/C
AD22105	THERMOSTAT, RESISTOR PROGRAMMABLE, 1 OPEN COLLECTOR 1 mA OUTPUT, RANGE -40 TO +125C, $V_s=+2.7$ to +7V, $I_q=75\mu A$ , Accuracy=2C, 4C hysteresis
AD22151	MAGNETIC, HALL EFFECT, LINEAR OUTPUT, $V_s=+4.5$ to 6V @ 6mA, Gain Adjustable from 2 to 6mV/Gauss, Ratiometric output to $V_{cc}$ , Adj. offset to uni/bipolar, -40 to +130C
AD246	CLOCK OSCILLATOR TO POWER AD204 ISOLATION AMP, See AD204 D/S for Specs
AD260	5 Channel LOGIC ISOLATOR, CMV=4KV, 20MHZ, Time Delay 29nsec, Edge to Edge Error=4nsec, +5V @ 7mA, w/3 Internal 3Watt Transformer
AD261	5 Channel LOGIC ISOLATOR, CMV=4 or 5KV, 20MHZ, Time Delay 29nsec, Edge to Edge Error=4nsec, +5V @ 7mA
AD2700	+10V, $V_{os}=2.5$ to 5mV, $TCV_{os}=3$ to 10ppm, +/-15VRails, See AD587 for new designs
AD2701	-10V, $V_{os}=2.5$ to 5mV, $TCV_{os}=3$ to 10ppm, +/-15VRails, See AD688 for new designs
AD2702	$\pm 10V$ , $V_{os}=2.5$ to 5mV, $TCV_{os}=3$ to 10ppm, $\pm 15V$ Rails, See AD688 for new designs
AD2710	+10V, AD2700 IN LOW COST PACKAGE, See AD587 for new designs
AD2712	$\pm 10V$ , AD2702, IN LOW COST PACKAGE, See AD688 for new designs
AD280	18 BIT 4 Channel (4 mV/V, T'Couple or 2 RTD's, $PGA=1$ to 128, On Chip $v_{ref}$ & Clk Osc., +5V, Bipolar Inputs
AD2S100	VECTOR ROTATION OF 3 PHASE or 2 PHASE SINE COSINE INPUTS , R/D and POSITION INPUTS, Parallel I/O, 3 PHASE COSINE OUTPUT OR SINE COSINE
AD2S105	VECTOR ROTATION OF 3 PHASE or 2 PHASE SINE COSINE INPUTS , R/D and POSITION INPUTS, Parallel I/O, SINE COSINE OUTPUT, See AD2S100
AD2S80A	10,12,14,16 BIT, USER PROGRAMMABLE, TRACKING RATE =16/1040RPM, +/-12V Rails @ 23mA, CERAMIC PACKAGE
AD2S81A	12 BIT, 30 ARC MIN ACCURACY, 28 PIN CERAMIC DIP
AD2S82A	10,12,14,16 BIT, USER PROGRAMMABLE, 2 to 22 arc minute accuracy, 44 PIN PLCC

[Click on a part to see Datasheet.](#)

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PART	DESCRIPTION
AD2S83	10,12,14,16 BIT, HIGH ACCURACY VELOCITY OUTPUT .25%, 44 LEAD PLCC
AD2S90	12 BIT, 375Hz tracking range, +/-5V Rail @ 7mA, INCREMENTAL ENCODER TYPE Serial I/O
AD2S99	TWO PHASE LOCKED SINE WAVES, PROGRAMMABLE, 2K to 20 KHZ
AD420	SINGLE, 16 BIT, PROGRAMMABLE VOLTAGE/CURRENT OUTPUT, 0 to 24mA, Serial I/O, +32V MAX RAIL
AD420EB	EVALUATION BOARD FOR AD420
AD421	SINGLE, 16 BIT, LOOP POWERED, "HART" COMPATIBLE. PROGRAMMABLE V/I 0 to 24mA OUTPUT, Iq=500uA, Serial I/O
AD515A	SINGLE, Vos=.1>3mV, Ib=75fA to 300fA, SR=0.3V/usec, Iq=1.5mA, See AD549 for new designs
AD521	RESISTER PROGRAMMABLE, See AD620/622/623 for new designs
AD5212	12 BIT, ±10V INPUT, 13usec, INTERNAL REF, See AD774 for new new designs
AD5215	12 BIT, ±10V INPUT, 13usec, NO REFERENCE, See AD774 for new designs
AD522	RESISTER PROGRAMMABLE, See AD620/622 for new designs
AD524	PIN PROGRAM G=1,10,100 & 1000, Vos RTI=50>200uV, Vos RTO=.5>2mV, 16/20 Pins
AD526	SOFTWARE PROGRAMMABLE G=1,2,4 & 8, Vos RTI=.5>1.5mV, SINGLE ENDED INPUT,
AD5300	SINGLE, 8 BIT, +2.7V, Iq=200uA, Ext Vref, Rail to Rail output, 3 Wire Serial I/O, w/Powerdown Iq=20/100nA, Power on Reset to Zero, 6 pin SOT-23, 8 uSOIC
AD53040	PIN DRIVER, -3V to +8V, 500Mhz UPDATE RATE,
AD53041	ACTIVE LOAD, 1.5nsec prop delay, linearity=0.12% FS, THREE SELECTABLE GAIN RANGES, 5/16/50mA
AD5310	SINGLE, 10 BIT, +2.7V, Iq=200uA, Ext Vref, Rail to Rail output, 3 Wire Serial I/O, w/Powerdown Iq=20/100nA, Power on Reset to Zero, 6 pin SOT-23
AD532	ACCURACY=1.0%, 4 QUADRANT, 1MHz BW, Vout=(X1-X2)*(Y1-Y2)/10, see AD633 for new designs
AD5320	SINGLE, 12 BIT, +2.7V, Iq=200uA, Ext Vref, Rail to Rail output, 3 Wire Serial I/O, w/Powerdown Iq=20/100nA, Power on Reset to Zero, 6 pin SOT-23
AD534	ACCURACY=0.25% , 1MHz BW, Vout=(X1-X2)*(Y1-Y2)/10-(Z1+Z2), 4 QUADRANT
AD536A	TOTAL ERROR mV +/-% of reading=2mV +/-0.2%, BW= 450 KHZ, FS=7V rms, w/dB OUTPUT

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD537	150 KHz, Vcc=+5V, Iq=2.5mA, Accuracy=.25% @100KHz, w/1mV/K TEMP SENSOR
AD538	ANALOG COMPUTATION UNIT, $V_{out}=V_y(V_z/V_x)^m$
AD539	ACCURACY=2.5%, ANALOG MULTIPLIER, DUAL Channel, CURRENT OUTPUT, 60MHz, $V_{out1}=-V_x*Y1$ , $V_{out2}=-V_x*Y2$ , 2 QUADRANT
AD542	SINGLE, Vos=2mV, Ib=50pA, SR=2V/usec, See AD711 for new designs
AD544	SINGLE, Vos=2mV, Ib=50pA, SR=8V/usec, See AD711 for new designs
AD545A	SINGLE, Eos=0.25 to 1mV, TC Vos=3 uV to 25uV, Ib=1pA, See AD549 for new designs
AD546	SINGLE, Vos=1mV, Ib=0.5pA, SR=2V/usec, See AD645A or AD795 for new designs
AD547	SINGLE, Vos=2mV, Ib=50pA, SR=2V/usec, eN=4uVpp, See AD711 for new designs
AD548	SINGLE, Vos=.25>2mV, Ib=10>20pA, SR=1V/usec, Iq=200uA
AD549	SINGLE, Ib=60fA to 250fA, Eos=0.25mV to 1mV, Iq=0.7mA, 2V/usec
AD5539	SINGLE, 1 GHZ, ALL NPN, Pins with NE5539
AD557	SINGLE, 7 BIT, COMPLETE, w/LATCH, Iq=23mA, +5V RAIL
AD558	SINGLE, 8 BIT, COMPLETE, w/LATCH, Iq=23mA, +5V RAIL
AD561	SINGLE, 10 BIT, CURRENT OUTPUT
AD565A	SINGLE, 12 BIT,CURRENT OUTPUT, INT Vref, 400nsec SETTling TIME to .01%, +/-12V Rails
AD566A	SINGLE, 12 BIT,CURRENT OUTPUT, EXT Vref, 350nsec SETTling TIME to .01%, +/-12V Rails
AD568	SINGLE, 12 BIT,CURRENT OUTPUT, 35nsec SETTling TIME to .01%. 350pV-SEC Glitch, See AD768 for new designs
AD569	SINGLE, 16 BIT, MONOTONIC, 4 Quadrant Multipling, See AD7846 for new designs
AD570	8 BIT, 25 usec, +5V & -12/15V RAILS, Parallel 8 BIT I/O, See AD7818 for new designs
AD571	10 BIT, 25 usec, +5V & -12/15V RAILS, Parallel 10 BIT I/O, See AD7818 for new designs
AD573	10 BIT, 25 usec, +5V & -12/15V RAILS, Parallel 8/16 BIT I/O, See AD7776 for new designs
AD574A	12 BIT, INDUSTRY STANDARD ENCODER, 25 usec, See AD674B or AD1674 for new designs

[Click on a part to see Datasheet.](#)



# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD578	12 BIT, 3 usec, See AD9621 for new designs
AD580	+2.5V, 3 Terminal Series Regulator, Vos=10>75mV, TCvos=10>85ppm, Band Gap, See AD680 or AD780 for new designs
AD581	+10V, Vos=5>30mV, TCvos=5>30ppm, Band Gap, See AD587 for new designs
AD582	6 usec to .01 %, +/-15V @ 4.5mA, +/-10V Input Range, SSBW=1.5MHZ, Full Power BW=70KHZ
AD584	MULTI-TAP, +2.5,+5, +7.5, +10V, Band Gap
AD585	SINGLE, 2MHz, 3 usec to .01%, +/-10V INPUT RANGE
AD586	+5V, Vos=2>25mV, TCvos=2 >25 ppm, Buried Zener
AD587	+10V, Vos=5>10mV, TCvos=5 >20 ppm, Buried Zener
AD588	PIN PROGRAMMABLE, +10, +5, $\pm 5$ , -5, -10V, Buried Zener
AD589	+1.2V, Vos=15mV, TCvos=10>100ppm, Iq=50 uA, Band Gap
AD590	TEMPERATURE SENSOR, CURRENT OUTPUT I=1uA/DEGREE KELVIN, 3 PIN HEADER
AD592	TEMPERATURE SENSOR, CURRENT OUTPUT I=1uA/DEGREE KELVIN, 8 PIN PLASTIC DIP
AD594	AMPLIFIER, J TYPE THERMOCOUPLES, SCALE FACTOR=10mV/C
AD595	AMPLIFIER, K TYPE THERMOCOUPLES, SCALE FACTOR=10mV/C
AD596	AMPLIFIER, J TYPE THERMOCOUPLES, SET POINT CONTROLLER, SCALE FACTOR=10mV/C
AD597	AMPLIFIER, K TYPE THERMOCOUPLES, SET POINT CONTROLLER, SCALE FACTOR=10mV/C
AD598	LVDT, 5 WIRE, $V_{out}=(A-B)/(A+B)$ , Gain Error=1%, Gain Error Drift=50ppm,
AD600	DUAL, VARIABLE GAIN AMPLIFIER, 35 MHz, LINEAR IN dB, 0 to +40dB
AD602	DUAL, VARIABLE GAIN AMPLIFIER, 35 MHz, LINEAR IN dB, -10-+30dB
AD603	SINGLE, 90 MHz, VARIABLE GAIN, RESISTOR PROGRAMMABLE, -11dB/+31dB, or, +9dB/+51dB, LINEAR IN dB
AD604	DUAL, VGA WITH PREAMP, 40MHz, 0.85nV/sqrthz, +/- 5V @ 36mA 3mA in shutdown, 0 to 48db (Pre-Amp gain=+14dB,) or +6dB to +54dB Pre=Amp gain=20dB,
AD605	DUAL, VGA, 40 MHz, 1.8 nV/SQRTHz, SINGLE SUPPLY +5V @ 23mA, 3mA in shutdown, -14 dB to 48 dB or 0 dB to +48 dB/Channel

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD606	50 MHz, 80dB Range, Demodulating Log Amp w/ Limiter Output
AD607	GSM LINEAR IF SUB SYSTEM, +2.7V @ 8mA w/Powerdown Iq=550uA, MIXER (-15 ip3 1dB Comp. Pt, I/Q DEMODS 300KHZ.>22MHz IF, w/AGC Output, PLL QUADRATURE OSCILL., IF +18 ips -15 1dB C0mp Pt, 76dB Gain Control input 20mV/dB, 500MHz RF,
AD608	LIMITING IF SUBSYSTEM, RF=500MHz, IF=30MHz, MIXER, LOG LIMITING IF, RSSI, HARD LIMITED IF OUTPUT, -5dBm IP3
AD6190	SINGLE CHIP RADIO IC, 902-928MHZ, PART OF ZILOG PHONE REFERENCE DESIGN (GLOSSY), H3019-7.5-2/97
AD620	RESISTOR PROGRAMMABLE, RTO Error Av, 1.=025uV, TCvos=.6uV/C, Av=1 to 1000, LOW NOISE/POWER/COST
AD621	PIN PROGRAMMABLE, RTO Error Av, 1.=025uV, TCvos=.6uV/C, Av=10 to 100, LOW NOISE/POWER/COST
AD622	RESISTOR PROGRAMMABLE, FAST, ACCURATE, Av=1 to 1000, Gain Error=.15%, RTI Vos=250uV, RTO Vos=2mV, LOWEST COST
AD623	RESISTOR PROGRAMMABLE, Single Supply I/A, Rail to Rail Output, +3V >+/5V Rails, Ib=20nA, Eos=100uV, Iq=500uA, CMRR @ Av=10 93dB, CMV=0>3.5V w/+5V Rail
AD624	PIN PROGRAMMABLE or EXT Rgain SET, GAIN=1,100, 200, 500, 1000
AD625	RESISTER PROGRAMMABLE, GAIN 1 to 10,000, WIDE BANDWIDTH
AD626	PIN PROGRAMMABLE, RESISTER ATTEN. INPUT, Av=10 & 100, LOW NOISE/POWER/COST, SINGLE SUPPLY, LARGE CMV RANGE>5x Vcc
AD630	BALANCED MODULATOR/DEMODULATOR, 2MHz BANDWIDTH
AD632	ACCURACY=0.5%, 4 QUADRANT, 1MHz BW, Vout=(X1-X2)*(Y1-Y2)/10+Z, see AD633 for new designs
AD633	ACCURACY=2%, 4 QUADRANT, 1MHz BW, Vout=(X1-X2)*(Y1-Y2)/10+Z, see AD633 for new designs
AD636	TOTAL ERROR mV +/-% of reading=0.2mV +/-0.2%, BW= 1.0 MHz, FS=200 mV rms, w/dB OUTPUT
AD637	TOTAL ERROR mV +/-% of reading=0.5mV +/-0.3%, BW= 8 MHz, FS=7V rms, w/dB OUTPUT
AD639	TRIG FUNCTION GENERATOR, Can be configured to generate any trig function.
AD640	LOGARITHMIC AMPLIFIER, 120 MHz, 50dB RANGE, >70dB Range Cascading 2 AD640
AD6400	BASE BAND + RADIO CIRCUITRY FOR DECT: AD7011, AD7013, AD607 (GLOSSY), H3014-7.5-2/97

[Click on a part to see Datasheet.](#)

# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD6402	DECT IF TRANSCEIVER, +3.1V @30mA, High bit rate 1MHz, FM or FSK Modulation ,Mixer, IF Baseband filter/limiter, RSSI, VCO, PLL Demod, on chip power mgmt.
AD641	LOGARITHMIC AMPLIFIER, 250 MHz, 44dB RANGE, +/- 2dB LOG RSSI ACCURACY
AD642	DUAL, Vos=2mV, Ib=75pA, Iq=2.8mA, SR=2V/usec, See AD712/OP249 for new designs
AD6432	GSM IF TRANSCEIVER SUB SYSTEM, RF TO IF DOWN CONVERTER, 2 STAGE IF AMP w/AGC, TRANSMIT/RECEIVE QUAD MOD/DEMODO, IF UP MIXER FILTER & POWER AMP, +2.7V @13mA, Iq=8uA IN SHUTDOWN
AD6435	ADSL, DIGITAL INTERFACE & CONTROL, 1 of 4 CHIPS in the 2ND GENERATION, INTEGRATES AD6442 WITH MORE FUNCTIONALITY, interfaces ADSL Modem to external CO or RT Modem, does bit stuff, robbing,forward error, scramble, interleaving
AD6436	ADSL, DISCRETE MULTI-TONE ENGINE, 2ND GENERATION, REPLACES THE SHARC and AD6448, COMPLETE DMT BASEBAND ENGINE, w/HARDWIRED ADSP2181 for
AD6437	ADSL ANALOG FRONT END FOR 1ST & 2ND GENERATION,
AD644	DUAL, Vos=2mV, Ib=75pA, Iq=4.5mA, SR=8V/usec, See AD712/OP249 for new designs
AD645	SINGLE, Vos=0.5mV, TCVos=5uV/C, Ib=1pA, BB OPA111 2nD Source
AD6458	GSM LINEAR IF SUB SYSTEM, +3.0V @ 22mA w/Powerdown Iq=8uA, 400MHz MIXER AGC -8.8>+9.5dB, 2dB ip3, -11dB Comp. Pt, IF AMP AGC -9>+48dB, I/Q DEMODS 5>50MHz, w/AGC Input, NOT OPEN MARKET PART
AD6459	GSM LINEAR IF SUB SYSTEM, +2.7V @ 8mA w/Powerdown Iq=550uA, MIXER (0dB ip3 -11dB Comp. Pt, IF AMP, I/Q DEMODS 300KHZ.>22MHz IF, w/AGC Output, PLL QUADRATURE OSCILL., 76dB Gain Control input 20mV/dB, 500MHz RF,
AD647	DUAL, Vos=0.25>1mV, TCVos=2.5>10uV/C, Ib=35>75pA, SR=2V/usec, Iq=2.8mA
AD648	DUAL, Vos=0.3>2mV, TCVos=3>10uV/C, Ib=10>20pA, SR=1V/usec, Iq=400uA
AD650	1MHz, Vcc=+/-15V, Iq=8mA, Accuracy=.25% @100KHz, 100KHz F/V Mode
AD652	2MHz SYNCHRONOUS, Vcc=+/-15V, Iq=15mA, Accuracy=0.75>1.5% @2MHz, 100KHz F/V Mode
AD654	500 KHz, Vcc=+5V, Iq=2.5mA, Accuracy=.4% @500KHz
AD660	SINGLE, 16 BIT, AD669 w/Serial or 8 BYTE I/O & /ASYNCHRONOUS CLEAR,
AD6600	11 BIT, 8 BIT MANTISSA, 3 BIT EXPONENT, 20MSPS, INPUT CAN TRACT 200MHz Signal, DUAL Channel, (D/Range=92dB 30dB Variable Atten.+62dB A/D GAIN RANGING & RSSI, for NARROW BAND APPL.

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD6600/6620	DIVERSITY BASE STATION RECEIVER, FOR NEW AIR STANDARDS (GLOSSY), H3017-7.5-2/97
AD6620	DUAL Channel DECIMATING RECEIVER, 65MHz I or Q, 32.5MHz I&Q, w/COMPLEX NCO, QUADRATURE MIXER, 2 PRGM DECIMATION STAGES
AD664	QUAD, 12 BIT, 4 QUADRANT MULTIPLYING, Vs=+/-15V @ 19mA, I/O=Parallel 4/8/12
AD6640	12 BIT, 65MSPS, SFDR=80dB @ 30MHz,DIFF INPUT, +5V @ 90mA, TTL/CMOS output levels, Pins with AD9042 Except for Voffset Pin
AD6640/6620	MULTI-Channel/MULTI-MODE BASE STATION RECEIVER, FOR NEW AIR STANDARDS (GLOSSY), H3016-7.5-2/97
AD667	SINGLE, 12 BIT, SETTling TIME 4usec to 0.01%, Vref Int=+10V, +/-12V Rails
AD668	SINGLE, 12 BIT, 2 QUADRANT MULTIPLYING, SETTling TIME to 0.01%=120nsec,See AD768
AD669	SINGLE, 16 BIT, COMPLETE, $\pm 1$ LSB INL, SETTling TIME=13usec, 16 BIT LOAD
AD670	8 BIT, 10 usec,Vs= +5V, WITH PIP STRAP INPUT IN-AMP 2.5mV or 25mV/LSB, 8 BIT I/O
AD673	8 BIT, 30 usec, +5 & -12V RAILS, 8 BIT I/O
AD674B	12 BIT, AD574A UPGRADE, INDUSTRY STANDARD ENCODER, 15 usec
AD676	16 BIT 100 KSPS, (SNR+D=89>90dB, THD=-92>95dB, IMD=-102DB @ Fin=1KHZ) w/AUTO CAL, 16 BIT Parallel I/O, +/-12V Rails
AD677	16 BIT 100 KSPS, (SNR+D=89>90dB, THD=-92>95dB, IMD=-102DB @ Fin=1KHZ) w/AUTO CAL, 16 BIT Serial I/O, +/-12V Rails
AD678	12 BIT, 200 KSPS,+/-12V Rails ,( SNR +D=70dB, THD=-80dB @ Fin=10KHZ) 8 or 16 BIT I/O
AD680	+2.5V, Iq=250uA,Vos=50>100mV, TCVos= 25>50ppm
AD6816	155.52 MHz, ATM USER NETWORK INTERFACE to either UTP #5 or a FIBER OPTIC SYSTEM. Line Equalization, zero restore, line drive, clock recovery & data re-time
AD684	QUAD, 4MHz, 700 nsec to 0.01%, +/-5V INPUT RANGE
AD688	$\pm 10$ V, Vos=2>5mV, TCVos=3>6ppm, eN=6uVpp, Buried Zener
AD693	TRANSMITTER, LOOP POWERED, 4 to 20 mA OUTPUT, CALIBRATED 30/60mV RANGES, ACCURACY= 0.05%
AD694	TRANSMITTER, LOCALLY POWERED, REQUIRES EXTERNAL LOOP SUPPLY, 4 to 20 mA OUTPUT, CALIBRATED 2 and 10V RANGES, ACCURACY=0.05%

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD698	LVDT, 4 WIRE, $V_{out}=A/B$ , Gain Error=1%, Gain Error Drift=100ppm
AD7002	2 Channel SIGMA/DELTA A/D: 2, 10 BIT D/A, GSM BASEBAND I/O
AD7008	10 BIT, DDS, IF MODULATOR, 32 BIT PHASE ACCUMULATOR: w/PHASE & AMPLITUDE REGISTORS, 20MHz or 50 MHz, 10 BIT D/A
AD7010	BASEBAND TRANSMITTER DAC for RCR (JAPANESE) DIGITAL RADIO
AD7011	BASEBAND TRANSMITTER DAC for TIA (NORTH AMERICA) DIGITAL RADIO
AD7013	BASEBAND RECEIVER PORT for TIA IS-54, DIGITAL, or, ANALOG CELLULAR RADIO
AD7015	BASEBAND CODEC and VOICEBAND CODEC FOR GSM, +3V RAIL, FOR GSM AND PCS1900
AD704	QUAD, $V_{os}=25\mu V$ , $I_b=.15nA$ , $A_{ol}=0.2V/\mu V$ , $I_q=0.6mA$ , Super Beta Input Stage
AD705	SINGLE, LOW POWER, $V_{os}=25\mu V$ , $I_b=.15nA$ , $A_{ol}=0.2V/\mu V$ , $I_q=0.6mA$ , Super Beta Input Stage
AD706	DUAL, $V_{os}=25\mu V$ , $I_b=.15nA$ , $A_{ol}=0.2V/\mu V$ , $I_q=0.6mA$ , Super Beta Input Stage
AD707	SINGLE, IMPROVED OP-07, INDUSTRY STD, LOW DRIFT, See OP77, OP177
AD708	DUAL, IMPROVED OP-07 INDUSTRY STD, $E_{os}=15>90\mu V$ , $TCV_{os}=0.1>1\mu V/C$ , $I_b=1>2.5nA$
AD711	SINGLE, 3MHz, $16V/\mu sec$ , $I_b=25>50pA$ , $V_{os}=0.25>2mV$
AD7111	SINGLE, 8 BIT, LOG DAC, 88.5 dB range, .375 dB Steps, see AD7111A
AD7111A	SINGLE, 8 BIT, LOG DAC, 88.5 dB range, .375 dB Steps, LOW GLITCH
AD7112	DUAL, 8 BIT, LOG DAC, 88.5 dB range, 1.5 dB Steps, AD7528 PINOUT
AD712	DUAL, 3MHz, $16V/\mu sec$ , $I_b=50>75pA$ , $V_{os}=0.3>3mV$
AD713	QUAD, 3MHz, $16V/\mu sec$ , $I_b=75>150pA$ , $V_{os}=0.5>1.5mV$
AD722	ANALOG RGB to NTSC or PAL ENCODER , +5V RAIL, w/ON CHIP DC RESTORE and SUB CARRIER CLOCK OSCILLATOR
AD7224	SINGLE, 8 BIT, $V_{out}=10V$ , 1 External $V_{ref}$ , $V_{dd}=+15V @ 6mA$ , $V_{ss}=-5V 7 3mA$ , or Single Supply, 8 BIT I/O, Double Buffered
AD7225	QUAD, 8 BIT, $V_{out}=10V$ , 4 External $V_{ref}$ , $V_{dd}=+15V @ 6mA$ , $V_{ss}=-5V 7 3mA$ , or Single Supply, 8 BIT I/O, Double Buffered

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7226	QUAD, 8 BIT, Vout=10V, 1 External Vref, Vdd=+15V @ 6mA, Vss=-5V 7 3mA, or Single Supply, 8 BIT I/O, Double Buffered
AD7228	OCTAL, 8 BIT, VOLTAGE OUTPUT, see AD7228A
AD7228A	OCTAL, 8 BIT, VOLTAGE OUTPUT, 1 Vref input, +5V or +5/-15V RAILS, Iq=14mA
AD723	ANALOG RGB to ANALOG NTSC or PAL ENCODER , +5V RAIL, AC COUPLED. Delay Line, Luma trap, Inputs terminated when monitor is unplugged
AD7233	SINGLE, 12 BIT, +/-15V Rails, Int. +5V Vref, 8 PINS, Serial I/O
AD7237	DUAL, 12 BIT, VOLTAGE OUTPUT, 8 BIT I/O, See AD7237A
AD7237A	DUAL, 12 BIT, +/-12V Rails, Int +5V Vref, Voltage Output=+/- 5V, Double Buffered, 8 Bit I/O
AD724	ANALOG RGB to NTSC or PAL ENCODER , +5V RAIL, w/OFF CHIP DC RESTORE CAPS to improve clamp time constant, w/ S'Down & ON-BOARD 4FSC SUB CARRIER CLOCK OSCILLATOR, Low Jitter 14nsec
AD7242	DUAL, 12 BIT, Int +3V Vref, Double Buffered, Serial I/O, +/-3V VOLTAGE OUTPUT
AD7243	SINGLE, 12 BIT, Int +3V Vref, Double Buffered, Serial I/O, +/-3V VOLTAGE OUTPUT, w/Readback & Reset
AD7244	DUAL, 14 BIT, Int +3V Vref, Double Buffered, Serial I/O, +/-3V VOLTAGE OUTPUT
AD7245	SINGLE, 12 BIT, COMPLETE, 12 BIT I/O, see AD7245A
AD7245A	SINGLE, 12 BIT, +/-12V Rails, Int +5V Vref, Voltage Output=+/- 5V, Double Buffered, 12 Bit I/O
AD7247	DUAL, 12 BIT, VOLTAGE OUTPUT, AD7237, 12 BIT I/O, see AD7247A
AD7247A	DUAL, 12 BIT, +/-12V Rails, Int +5V Vref, Voltage Output=+/- 5V, Double Buffered, 12 Bit I/O
AD7248A	SINGLE, 12 BIT, +/-12V Rails, Int +5V Vref, Voltage Output=+/- 5V, Double Buffered, 8 Bit I/O
AD7249	DUAL, 12 BIT, SELECTABLE OUTPUTS, SINGLE or DUAL SUPPLY, +12 or +15, Serial I/O
AD725	ANALOG RGB to NTSC or PAL ENCODER , Vout=2x Normal output for driving reverse terminated cable, Low Cost
AD7302	DUAL, 8 BIT, +2.7V, Iq=2mA, Ext Vref, Powerdown (1uA), Rail to Rail output, Parallel I/O, w/Clear & Power on Reset, 20 pin
AD7303	DUAL, 8 BIT, +2.7V, Iq=2mA, Ext Vref, Powerdown (1uA), Rail to Rail output, Serial I/O, w/Power on Reset, 8 pin
AD7304	QUAD, 8 BIT, +2.7V, +5 or +5V, Rail to Rail, 3 Wire Serial I/O SPI Comp., w/Sdown & CLR, 4 Vref Inputs

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7305	QUAD, 8 BIT, +2.7V, +5 or +5V, Rail to Rail, Parallel I/O., w/Sdown & CLR, 1 Vref Inputs, pins w/AD7226
AD7306	RS-232/422, MODE 1 DRIVERS, 1 EACH RECEIVERS 2 RS232 1 RS422, MODE 2 DRIVERS, 1 EACH RECEIVERS 1 RS232 2 RS422, RS232=100KBPS, RS422=5000KBPS
AD73311	SINGLE, 16 BIT, A/D/A Codec, +3V 20mW, 65dB SNR, Fs=2/4/16Mhz, SAMPLE RATE MAX=64KSPS, A/D Gain Range=3dB, D/A=21dB
AD73322	DUAL, 16 BIT, A/D/A Codec, +3V 40mW, 65dB SNR, SAMPLE RATE=64/16/8KSPS, A/D Gain Range=38dB, D/A=6dB
AD7339	8 BIT A/D, 2 MSPS, 640KHZ ABW @ IF 3/6MHZ, DUAL Parallel 8 BIT D/A, DUAL Serial 8 BIT D/A, +5V @ 45mA
AD734	ACCURACY, 0.4%, Low Noise, 10MHz BW, $V_{out}=(X1-X2)*(Y1-Y2)/(U1-U2)-(Z1-Z2)$ , 4 QUADRANT
AD736	TOTAL ERROR mV +/-% of reading=0.3mV +/-0.3%, BW=450 KHZ, FS=200 mV rms, LOW POWER, current output
AD737	TOTAL ERROR mV +/-% of reading=0.2mV +/-0.32%, BW= 450 KHZ, FS=200 mV rms, LOW POWER, voltage output
AD7376	SINGLE, 7 BIT, LOOKS LIKE 3 TERMINAL POTENTIOMETER 10K, 50K, 100K, 1M,+5V or +/-15V RAILS, 3 Wire serial I/O, w/Sdown
AD7390	SINGLE, 12 BIT, Rail to Rail, +2.7 to +5.5V Rail, Vref ext, Iq=100uA, Serial I/O, w/Clear & S/down, 8 pin
AD7391	SINGLE, 10 BIT, Rail to Rail, +2.7 to +5.5V Rail, Vref ext, Iq=100uA, Serial I/O, w/Clear & S/down, 8 pin
AD7392	SINGLE, 12 BIT, Rail to Rail, +2.7 to +5.5V Rail, Vref ext, Iq=100uA, Parallel I/O, w/Sdown Iq=1uA, w/Reset, 1 Buffer, Pins with AD7393
AD7393	SINGLE, 10 BIT, Rail to Rail, +2.7 to +5.5V Rail, Vref ext, Iq=100uA, Parallel I/O, w/Sdown & Reset, 1 Buffer, Pins with AD7392
AD7394	DUAL, 12 BIT, Rail to Rail, +2.7 to +5.5V Rail, Vref ext, Iq=100uA, SPI Serial I/O, w/Sdown & Reset, 2 Buffers, Pins with AD7395
AD7395	DUAL, 10 BIT, Rail to Rail, +2.7 to +5.5V Rail, Vref ext, Iq=100uA, SPI Serial I/O, w/Sdown & Reset, 2 Buffers, Pins with AD7394
AD7396	DUAL, 12 BIT, Rail to Rail, +2.7 to +5.5V Rail, Vref ext, Iq=200uA, Parallel I/O, w/Sdown Iq=1uA, & Reset, 2 Buffers, Pins with AD7397
AD7397	DUAL, 10 BIT, Rail to Rail, +2.7 to +5.5V Rail, Vref ext, Iq=200uA, Parallel I/O, w/Sdown Iq=1uA, & Reset, 2 Buffers, Pins with A7396
AD7398	QUAD 12 BIT, Rail to Rail Output, +5.0V Rail, Vref ext, Iq=800uA, Serial SPI I/O, w/Sdown & 2 Buffers

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD741	SINGLE, GENERAL PURPOSE, INDUSTRY STD. See OP291 for new designs
AD7416	10 BIT, +3V or +5V @ 1.5mA, 0.1MSPS, I2C Serial I/O, AUTO POWER DOWN, 4/40/4000uW @10SPS/1KSPS/10KSPS, INT Vref=2.5V, Over Temp & Alarm Registers, 8 PINS, LM75 Pinout
AD7417	10 BIT, 5 Channel, (1 CH to on chip temp sensor), +3V or +5V @ 1.5mA, 0.1MSPS, I2C Serial I/O, AUTO POWER DOWN, 4/40/4000uW @10SPS/1KSPS/10KSPS, INT Vref=2.5V, Over Temp Register, 16 PINS
AD7418	10 BIT, 2 Channel, (1 CH to on chip temp sensor), +3V or +5V @ 1.5mA, 0.1MSPS, I2C Serial I/O, AUTO POWER DOWN, 4/40/4000uW @10SPS/1KSPS/10KSPS, INT Vref=2.5V, Over Temp Register, 8 PINS
AD743	SINGLE, LOW NOISE, Vos=0.5>1mV, Ib=400pA, SR=2.8V/uSec, En=0.38nVpp
AD744	SINGLE, 8MHz (EXT COMPENSATION), 45V/uSec, Ib=50pA, Vos=0.25>2mV
AD745	SINGLE, LOW NOISE, Vos=0.5>1mV, Ib=400pA, Min Av=5, SR=12.5V/uSec, En=0.38nVpp
AD746	DUAL, Min Av=2, 13MHz, 75V/uSec, Ib=50>75pA, Vos=0.3>3mV
AD75004	QUAD, 12 BIT, VOLTAGE OUTPUT, +/-12V Rails, Int +5V Vref, 4 uSec, 8 BIT I/O
AD7501	8 Channel, 300 ohm, SINGLE ENDED, See ADG408 or ADG438F
AD75019	16x16 CROSS POINT SWITCH, Ron=500 ohms, Isolation 69dB @20KHZ, +/-12V Rails @ 1mA
AD7502	4 Channel, 300 ohm, DIFFERENTIAL, See ADG408 or ADG438F
AD7503	8 Channel, 300 ohm, SINGLE ENDED, See ADG408 or ADG438F
AD7506	16 Channel, 450 ohm, SINGLE ENDED, See ADG406, NOT LATCHED
AD7507	8 Channel, 450 ohm, DIFFERENTIAL, See ADG407, NOT LATCHED
AD75089	OCTAL, 12 BIT, VOLTAGE OUT, $\pm 5$ ,
AD7510DI	QUAD, SPST, 300 ohm, NEGATIVE TRUE I/O, OVERVOLTAGE PROTECTED +/-25V
AD7511DI	QUAD, SPST, 300 ohm, POSITIVE TRUE I/O, OVERVOLTAGE PROTECTED +/-25V
AD7512DI	DUAL, SPST, 300 ohm, OVERVOLTAGE PROTECTED +/-25V
AD7523	SINGLE, 7 BIT, SINGLE, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT
AD7524	SINGLE, 8 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT
AD7528	DUAL, 8 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT, +5V or +15V Supply, not TTL @ +15V

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7533	SINGLE, 10 BIT, INVERTED R2R, Ext +/- Vref, 3RD GENERATION, CURRENT OUTPUT
AD7534	SINGLE, 14 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT, 8 BIT I/O
AD7535	SINGLE, 14 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT, 8/14 BIT I/O
AD7536	SINGLE, 14 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT, 8/14 BIT I/O, INT. 4Q MULT. RESISTERS
AD7537	DUAL, 14 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT, 8 BIT I/O
AD7538	SINGLE, 14 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT, 14 BIT I/O
AD7541A	SINGLE, 12 BIT, SINGLE, 12 BIT, INVERTED R2R, Ext +/- Vref, 2ND GENERATION, See AD7945
AD7542	SINGLE, 12 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT, 4 BIT I/O
AD7543	SINGLE, 12 BIT, INVERTED R2R, Ext +/- Vref, CURRENT OUTPUT, Serial I/O, See AD7943
AD7545	SINGLE, 12 BIT, INVERTED R2R, CURRENT OUTPUT, 12 BIT I/O, TTL, Vdd=+5V, see AD7945
AD7545A	SINGLE, 12 BIT, INVERTED R2R, Ext +/- Vref, w/12 Bit Latch, See AD7945
AD7547	DUAL, 12 BIT, INVERTED R2R, CURRENT OUTPUT, 12 BIT I/O
AD7548	SINGLE, 12 BIT, INVERTED R2R, CURRENT OUTPUT, 8 BIT I/O, See AD7948
AD7549	DUAL, 12 BIT, INVERTED R2R, CURRENT OUTPUT, Gain Error=3Lsb's, Double Buffered, 4 BIT I/O, Recommended for New Designs
AD7564	QUAD, 12 BIT, INVERTED R2R, CURRENT OUTPUT, DOUBLE BUFFERED, 4 Ext Vref, +5V Rail
AD7568	OCTAL, 12 BIT, INVERTED R2R, CURRENT OUTPUT, DOUBLE BUFFERED, 4 Ext Vref, +5V Rail
AD7569	8 BIT 2 usec A/D, Single D/A Voltage Output 2usec, +5V or +/-5V Rails
AD7572	12 BIT, 5 or 10 usec, see AD7572A
AD7572A	12 BIT, 3, 5, 10 usec Versions, Int. Vref=+2.5V, +5 & -15V Rails
AD7574	8 BIT, 15usec, +5V RAIL, 8 BIT I/O, Iq=3mA,
AD7575	8 BIT, w/Track & Hold, +5V RAIL, 8 BIT I/O, 200KSPS, FPB=50KHZ, Vref=ext +2.5V
AD7576	8 BIT, w/Track & Hold, +5V RAIL, 8 BIT I/O, 360KSPS, FPB=50KHZ, Vref= INT +1.2V
AD7578	12 BIT, 100 usec, +/-5VRails, Iq=7.5mA, AUTO ZERO,

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7579	10 BIT, 20 usec, SINGLE +5V RAIL, 8 BIT I/O
AD7581	8 BIT, 66 usec, 8 Channel MUX, 8x8 Dual Ported RAM, +5V, REQUIRES -10V Vref, See AD7829 for new designs
AD7582	12 BIT, 100 usec, AUTO ZERO, WITH 4 Channel MUX
AD7590DI	QUAD, SPST, AD7510 WITH A LATCH, OVERVOLTAGE PROTECTED +/-25V, POSITIVE TRUE
AD7592DI	DUAL, SPDT, AD7512 WITH A LATCH, OVERVOLTAGE PROTECTED +/-25V
AD760	SINGLE, 16/18 BIT, SELF CALIBRATING, ASYNC CLEAR, HARD ZERO OUTPUT ON POWER UP/DOWN, Serial/BYTE I/O
AD7628	8 BIT, DUAL, INVERTED R2R, CURRENT OUTPUT, TTL @, +12V or +15V Supply
AD766	SINGLE, 16 BIT, SETTLING TIME=1usec, SNR=94 dB, THD=-81dB, Serial DSP COMPATABLE INTERFACE
AD7669	8 BIT 2 usec A/D, DUAL DAC 1usec, +5V Rail
AD767	SINGLE, 12 BIT, Vs=+/-12V @ 13mA, SETTLING TIME=4usec, Vout is pin strappable=+/-2.5/5/10V
AD7672	12 BIT, FASTER 7572, 5/10 usec, NO INT. REFERENCE
AD768	SINGLE, 16 BIT, DDS DAC, 40MSPS, 14 BIT ACCURATE, ULTRA LOW GLITCH=35 pV=sec, See AD768B
AD7701	16 BIT, 0.1 to 10 Hz CLOCK PROGRAMMABLE 6 POLE GAUSSIAN (BRICK WALL) FILTER
AD7703	20 BIT, 0.1 to 10 Hz CLOCK PROGRAMMABLE 6 POLE GAUSSIAN (BRICK WALL) FILTER
AD7705	16 BIT, PGA GAIN 1 to 128, 2 DIFF CH's, Software Selectable input Buffer, 3 WIRE Serial I/O, +3V @ Iq=450uA
AD7706	16 BIT, PGA GAIN 1 to 128, 3 Single Ended CH's, Software Selectable input Buffer, 3 WIRE Serial I/O, +3V @ Iq=450uA
AD7710	24 BIT, SIGNAL CONDITIONER FOR THERMOCOUPLES, FRONT END, SINX/X ^3 FILTER
AD7711	24 BIT, SIGNAL CONDITIONER, RTD FRONT END, SOFTWARE PRGM (SINX/X)^3X FILTER
AD7711A	24 BIT, SIGNAL CONDITIONER, RTD FRONT END, SOFTWARE PRGM (SINX/X)^3 FILTER
AD7712	24 BIT, SIGNAL CONDITONE for mV INPUTS, SOFTWARE PRGM (SINX/X)^3 FILTER
AD7713	24 BIT, SIGNAL CONDITONE for RTD INPUTS, SOFTWARE PRGM (SINX/X)^3 FILTER, Iq=300uA
AD7714	24 BIT, 3 Channel DIFF or 5 Channel SINGLE ENDED, Dash-5= +5V RAIL, Dash-3= +3V RAIL, Iq=150uA, Iq=10uA in shutdown, 18.5 BITS @ 26HZ, SOFTWARE PRGM (SINX/X)^3 FILTER

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7715	16 BIT, Dash-5= $\pm$ 5V, Dash-3= $\pm$ 3V, 0.0015% LINEARITY, 3 WIRE Serial, $I_q$ =500uA, PG=1/2/32/128, SOFTWARE PRGM (SINX/X) <sup>3</sup> FILTER
AD7716	22 BIT, 4 Channel, 36.5 to 584HZ PROGRAMMABLE B/WIDTH, 18 BITS @ 584 HZ, SOFTWARE PRDM (SINX/X) <sup>3</sup> FILTER
AD7720	SIGMA/DELTA MODULATOR, 7th ORDER, 12.5MHz Master Clock, on chip vref=2.5V, +5V
AD7721	12 BIT ACCURATE 16 BIT RESOLUTION, SIGMA DELTA $F_c$ =15MHz, 468.75 KSPS, 229.2 KHZ ANALOG BW, +5V, 16 BIT Serial/12 BIT Parallel I/O, SELF CALIBRATION, w/SHUTDOWN
AD7722	16 BIT, SIGMA DELTA, 25MHz= $F_s$ , 195 KSPS, 90.6 KHZ ANALOG BW, +5V, SELF CALIBRATION OF OFFSET & GAIN, INT Vref= $\pm$ 2.5V, IO=Serial & Parallel
AD7723	16 BIT, SIGMA DELTA, 33MHz= $F_s$ , 1.2MSPS, 470 KHZ ANALOG BW, +5V, SELF CALIBRATION OF OFFSET & GAIN, IO=Serial & Parallel, w/Stand by Mode
AD7729	DUAL, 15 BIT, 270KSPS, GSM Baseband Rcv. Serial Port, FIR Digital Filters, 60dB SNR & THD, OFFSET Self Cal., w/POWER DOWN, 10 BIT AUXDAC
AD7730	19 BIT, 2 Channel DIFF, OPTIMIZED FOR WEIGH SCALE/PRESSURE TRANSDUCERS, w/PGA, 6 BIT "TARE" DAC, SYS/SELF CALIBRATION, TVCos=5nV/C, GainTC=2ppm/C, SOFTWARE PRGM (SINX/X) <sup>3</sup> FILTER
AD7730L	18 BIT, FLICKER FREE, 2 Channel DIFF, OPTIMIZED FOR WEIGH SCALE/PRESSURE TRANSDUCERS, w/PGA, 6 BIT "TARE" DAC, SYS/SELF CALIBRATION, TVCos=5ppm/C, GainTC=2ppn/C, SOFTWARE PRGM (SINX/X) <sup>3</sup> FILTER, LOW POWER
AD7731	24 BIT, 16 BIT pp @ 800Hz Update Rate, Update Rate max=6.4KHz, PGA=1,2,4,8,16,32 & 64, 2 Diff Inputs, TCvos=0.5nV/C, Gain TC=5ppm, System/Self Calibration, +5V, $I_q$ =18mA
AD7741	2.5MHz SYNCHRONOUS V/F, Fclk=100KHz min to 5 MHz max, Linearity=012%, +5V @ 8mA,Vref=Ext $\pm$ 2.5V, Single Ended Input, 1 Channel, Gain=1, 8 Pins
AD7742	2.5MHz SYNCHRONOUS V/F, Fclk=100KHz min to 5 MHz max, Linearity=012%, +5V @ 8mA,Vref=Int $\pm$ 2.5V, Gain=1 or 2, UNI/BP Inputs , 2CH Diff or 3 PseudoDiff CH, 116 Pins
AD774B	12 BIT, AD574A UPGRADE, INDUSTRY STANDARD ENCODER, 5 usec
AD775	8 BIT, 20MSPS, +5V @17mA, 2nd SOURCE CXD1175A, TMC1175, See AD9057 for new designs
AD7750	PRODUCT TO FREQUENCY CONVERTER, 2 CH INPUT w/PGA =1 or 16, w/A/D /CH, Fout =1.53 or 98.31Hz, +5V, Int Vref
AD7751	PRODUCT TO FREQUENCY CONVERTER, 2 CH INPUT , w/ANTI=PILFERING, ANALOG CALIBRATION

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7752	PRODUCT TO FREQUENCY CONVERTER, 2 CH INPUT , w/ANTI-PILFERING, SPI INTERFACE
AD7769	8 BIT, SERVO DE-MOD, 2 Channel 2.5 usec, A/D, DUAL D/A 2.5usec, +12 * +5V Rails, voltage output
AD7774	8 BIT, 4 Channel 3.6 usec A/D, DUAL 8 BIT D/A, SINGLE 11 BIT D/A, +5 & +12V, ONLY PLCC AVAILABLE
AD7776	10 BIT, 200 KSPS, SINGLE Channel
AD7777	10 BIT, 2.5usec, ACQUIRE 2 CH VIA DUAL T/H , +5V, 4 Channel MUX
AD7778	10 BIT, 2.5usec, ACQUIRE 2 CH VIA DUAL T/H , +5V, 8 Channel MUX
AD779	14 BIT, 128 KSPS,( SNR+D=78dB, THD=-84dB @ 10KHZ Fin), +/-12V Rails, Linear Input Bandwidth=500KHZ), 16 BIT I/O, See AD7851
AD780	+2.5V or +3.0V Pin Strappable, Vos=1>5mV, TC Vos=7 to 20 ppm, VERY LOW WIDE BAND NOISE Using Noise Reduction Pin
AD7801	SINGLE, 8 BIT, +2.7V or +5V, Iq=2mA, Parallel I/O, Rail to Rail Output, EXT Vref or Vdd/2 via Mux, w/P'down Iq=1uA & Power on Reset,
AD7804	QUAD, 10 BIT, Vout=Vbias +/- Vswing, 4 Vbias Modes, w/SUBDAC/CH to shift V bias, Serial I/O ,p/DOWN, p/ON RESET, ON BOARD REFERENCE/OP-AMP's
AD7805	QUAD, 10 BIT, Vout=Vbias +/- Vswing, 4 Vbias Modes, w/SUBDAC/CH to shift V bias, Parallel I/O, p/DOWN, p/ON RESET, ON BOARD REFERENCE/OP-AMP's
AD7808	OCTAL, 10 BIT, Vout=Vbias +/- Vswing, 4 Vbias Modes, w/SUBDAC/CH to shift V bias, Serial I/O ,p/DOWN, p/ON RESET, ON BOARD REFERENCE
AD7809	OCTAL, 10 BIT, Vout=Vbias +/- Vswing, 4 Vbias Modes, w/SUBDAC/CH to shift V bias, Parallel I/O, p/DOWN, p/ON RESET, ON BOARD REFERENCE
AD781	SINGLE, 4MHz, 700 nsec to 0.01%, Internal Cap, FPBW=1MHZ, 1mV/usec DROOP RATE, +/-5V INPUT RANGE
AD7810	10 BIT, +2.7V or +5V @ 3mA, 0.35MSPS, Serial DSP/uC I/O, AUTO POWER DOWN (200uW @ 10KSPS), EXT Vref=1.2V, 8 PINS
AD7811	10 BIT, 4 Channel, +2.7V or +5V @ 2.55mA, 0.5MSPS, Serial I/O Allows 2 AD7811 to share bus, 2 POWER DOWN MODES, INT Vref=1.2V
AD7812	10 BIT, 8 Channel, 2.7V or +5V @ 2.55mA, 0.5MSPS, Serial I/O Allows 2 7811 to share bus, 2 POWER DOWN MODES, INT Vref=1.2V
AD7813	8/10 BIT, +2.7V or +5V @ 2.55mA, 0.4MSPS, 8 bit Parallel I/O TRI-STATE, AUTO POWER DOWN (200uW @ 10KSPS), EXT Vref=1.2V

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7816	10 BIT, +3V or +5V @ 1.5mA, 0.1MSPS, SPI Serial I/O, AUTO POWER DOWN, 4/40/4000uW @10SPS/1KSPS/10KSPS, INT Vref=2.5V, Over Temp Register, 8 PINS
AD7817	10 BIT, 5 Channel, (1 CH to on chip temp sensor), +3V or +5V @ 1.5mA, 0.1MSPS, SPI Serial I/O, AUTO POWER DOWN, 4/40/4000uW @10SPS/1KSPS/10KSPS, INT Vref=2.5V, Over Temp Register, 16 PINS
AD7818	10 BIT, 2 Channel, (1 CH to on chip temp sensor), +3V or +5V @ 1.5mA, 0.1MSPS, SPI Serial I/O, AUTO POWER DOWN, 4/40/4000uW @10SPS/1KSPS/10KSPS, INT Vref=2.5V, Over Temp Register, 8 PINS
AD7819	8 BIT, +2.7V or +5V @ 5mA, 200KSPS, Parallel I/O w/3 STATE OUTPUTS, AUTO POWER DOWN (750uW @ 10kps), EXT Vref=1.2V, 16 PINS
AD7820	8 BIT, 1.36 usec, 1/2 Flash, +5V @ 20mA, Input Tracking Rate=0.2V/usec
AD7821	8 BIT, 0.7 usec, 1/2 Flash, +5V @ 20mA, SNR=40dB, THD=50dB
AD7822	8 BIT, +3V or +5V, 2MSPS, on chip +2.5V Vref, Parallel I/O, ADJUSTABLE Vmid to +1.5V, w/AUTO & MANUAL POWER DOWN, w/POWER SUPPLY MONITOR
AD7823	8 BIT, +2.7V or +5V @ 2.5mA, 0.135MSPS, Serial DSP/uC I/O, AUTO POWER DOWN (200uW @ 10KSPS), EXT Vref, 8 PINS
AD7824	8 BIT, 4 Channel, 2.5 usec, 1/2 Flash, +5V @ 20mA, Input Tracking Rate=0.157V/usec, Parallel I/O
AD7825	8 BIT, 4 CH, +3V or +5V, 2MSPS, on chip +2.5V Vref, Parallel I/O, ADJUSTABLE Vmid to +1.5V, w/AUTO & MANUAL POWER DOWN, w/POWER SUPPLY MONITOR
AD7827	8 BIT, +3V or +5V, 360nsec, on chip +2.5V Vref, Serial DSP/uC I/O, 8 Pins
AD7828	8 BIT, 8 Channel, 2.5 usec, 1/2 Flash, +5V @ 20mA, Input Tracking Rate=0.157V/usec, Serial I/O
AD7829	8 BIT, 8 CH, +3V or +5V, 2MSPS, on chip +2.5V Vref, Parallel I/O, ADJUSTABLE Vmid to +1.5V, w/AUTO & MANUAL POWER DOWN
AD783	SINGLE, 15MHz, 250 nsec to 0.01%, Internal Cap, Droop Rate=1uV/usec, FPBW=2MHZ, +/-3V Input Range
AD7834	QUAD, 14 BIT, VOLTAGE OUTPUT, +5V & +/-15V, Serial, POWER ON RESET, CLEAR TO PRESET VOLTAGE, Vout=+/-8.192V
AD7835	QUAD, 14 BIT, VOLTAGE OUTPUT, +5V & +/-15V, Parallel, POWER ON RESET, CLEAR TO PRESET VOLTAGE, Vout=+/-8.192V
AD7836	QUAD, 14 BIT, VOLTAGE OUTPUT, +5V & +/-15V, Parallel, POWER ON RESET, CLEAR TO PRESET VOLTAGE, Vout=+/-10V from input Ref=+/-5V
AD7837	DUAL, 12 BIT, VOLTAGE OUTPUT, EXTERNAL Vref, 8 BIT I/O

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7838	OCTAL, 13 BIT, +/-5V Rails, Vout=+/-4.5V, Power on Reset, 5usec Settling, SECOND SOURCE TO MAX547
AD7839	OCTAL 13 BIT, +/-15V & +5VRails @ 14mA, 3 Sets of Vrehi and Vreflo, 5usec, Parallel I/O, Power on Reset, Rail to Rail Outputs, Output Summing Amp Input resistor Available in pairs of 2.
AD7840	SINGLE, 14 BIT, COMPLETE, 14 BIT or Serial I/O
AD7841	OCTAL 14 BIT, +/-15V & +5VRails @ 14mA, 3 Sets of Vrehi and Vreflo, 5usec, Parallel I/O, Power on Reset, Rail to Rail Outputs, Output Summing Amp Input resistor Available in pairs of 2.
AD7845	SINGLE, 12 BIT, VOLTAGE OUT, 12 BIT I/O, INTERNAL 4Q MULT. RESISTERS
AD7846	SINGLE, 16 BIT, SINGLE, VOLTAGE OUTPUT, 4 QUADRANT MULTIPLYING, 16 BIT I/O, READBACK,
AD7847	DUAL, 12 BIT, VOLTAGE OUTPUT, EXTERNAL Vref, DOUBLE BUFFERED, 12 BIT I/O
AD7849	SINGLE, 14/16 BIT, A Grade=14 bits, B=16 bits, MULTIPLYING, OUTPUT CLAMPED TO 0V ON POWER UP & DOWN, Serial I/O
AD7851	14 BIT, 333/285KHZ PSEUDO DIFF w/ 2 INPUT RANGES, +3.3V @ Iq=2mA, R/W CAL DATA, w/SELF CAL & POWER/DOWN (5uW), Serial I/O, pins w/AD7853
AD7853	12 BIT, 200KSPS, +3 or +5V @ Iq=5.5mA, SELF CALIBRATION, POWER DOWN MODE, Serial I/O 3 WIRE SPI or 2 WIRE 8051
AD7853L	12 BIT, L Version=100KSPS, +3>5V @ Iq=1.6mA, SELF CALIBRATION, POWER DOWN MODE, Serial I/O 3 WIRE SPI or 2 WIRE 8051
AD7854	12 BIT, 200KSPS, +3 or +5V @ Iq=5.5mA, SELF CALIBRATION, POWER DOWN MODE, Parallel/Serial
AD7854L	12 BIT, 100KSPS, L Version= +3V @ Iq=1.8mA, SELF CALIBRATION, POWER DOWN MODE, Parallel I/O
AD7855	12 BIT, 600KSPS, +3 or +5V @ Iq=5.5mA, SELF CALIBRATION, POWER DOWN MODE, Parallel/Serial
AD7856	14 BIT, 333KSPS, 8 Channel, serial I/O, 24-pin SSOP & pin-comp with 12-bit 7858; +5V Iq=15mA, 5uS in sleep mode; 4.096V ref on-chip; w/system cal.
AD7858	12 BIT, 200KSPS, 8 Channel, +3 or +5V RAIL, SELF CALIBRATION, Iq=6mA, POWER DOWN MODE, Serial I/O, 3 WIRE SPI or 2 WIRE 8051
AD7858L	12 BIT, 100KSPS, 8 Channel, L Version=+3>5V RAIL, SELF CALIBRATION, Iq=1.6mA, POWER DOWN MODE, Serial I/O, 3 WIRE SPI or 2 WIRE 8051
AD7859	12 BIT, 8 Channel, 200KSPS, +3V or +5V, @ Iq=6mA, SELF CALIBRATION, POWER DOWN MODE, Parallel
AD7859L	12 BIT, 8 Channel, 100KSPS, L Version=+3>5V @ Iq=1.8mA, SELF CALIBRATION, POWER DOWN MODE, Parallel

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7861	11 BIT, 4 CH w/TRACK&HOLD, 3 AUX. INPUT Channels, +5V, 3.2usec/CH,
AD7862	12 BIT, DUAL, 2Channel, diff simultaneous, 250KBPS, -3= $\pm$ 2.5V, -10= $\pm$ -10V, -2=0 to +2.5 INPUT, Parallel I/O, +5V, Iq=12mA, Int +2.5V Vref
AD7863	14 BIT, DUAL, 193kSPS ADC (2x2 ch mux, 2 SHA's, 2 ADCs, ref,clock) for simultaneous sampling; Bipolar Inputs up to $\pm$ -10V, +5V Rail,Parallel oI/O, auto sleep mode Iq <1uA; pin-comp w/7862. , Unreleased New Product as of 8/15/97
AD7864	12 BIT, 1.65usec, 4-CH simultaneous sampling, -1= $\pm$ -10V, -2=+2.5V, -3=0>+2.5, parallel I/O, +5V@18mA, 25uW in s/down, Vlogic +3 or+5V, HW/SW random Channel selection.
AD7868	12 BIT, 83 KSPS, w/ T/H & Int Vref=+3V, Serial I/O, Vs= $\pm$ -5V @ 12/27mA
AD7869	14 BIT, 83 KSPS, w/ T/H & Int Vref=+3V, Serial I/O, Vs= $\pm$ -5V @ 12/27mA
AD7870	12 BIT, 100 KSPS, $\pm$ 3V INPUT, $\pm$ 5V Rails
AD7870A	12 BIT, 100 KSPS, $\pm$ 3V INPUT, $\pm$ 5V Rails, WITH SINGLE INPUT DIGITAL CONTROL
AD7871	14 BIT, 83 KSPS, $\pm$ -5V Rails ,( SNR +D=80dB, THD=-86dB @ Fin=10KHZ) 14 BIT I/O
AD7872	14 BIT, 83 KSPS, $\pm$ -5V Rails ,( SNR +D=80dB, THD=-86dB @ Fin=10KHZ) SERIAL I/O
AD7874	12 BIT 8/usec/CH, 29 KSPS THROUGHPUT, w/4 SIMULTANEOUS T/H, $\pm$ -5V Rails
AD7875	12 BIT, AD7870 100 KSPS, 0 to +5V INPUT
AD7876	12 BIT, AD7870 100 KSPS, $\pm$ 10V INPUT
AD7878	12 BIT 100 KSPS, $\pm$ -5V Rails, 8 WORD FIFO, FAST DSP INTERFACE 41nsec ACCESS TIME
AD7880	12 BIT, 66 KSPS, (SNR+D 70dB, THD=-80dB, IMD=-80dB @ Fin=1KHZ), 12 BIT I/O, +5V@ Rail
AD7883	12 BIT, 66 KSPS, 12 BIT I/O, +3V
AD7884	16 BIT, 166 KSPS, (SNR+D=84dB, THD=-88dB, IMD=-84dB @ Fin=1KHZ), $\pm$ -5V @ 30mA, 16 BIT I/O
AD7885	16 BIT, 166 KSPS, (SNR+D=84dB, THD=-88dB, IMD=-84dB @ Fin=1KHZ), $\pm$ -5V @ 30mA, 8 BIT I/O or Serail
AD7886	12 BIT, 750 KSPS, See AD9221 for new designs
AD7887	12 BIT, 2 Channel, A/D, 4.6usec, 400nsec T/H, ref on-chip, +2.0V to 5.25V @ 0.7/.45mA 200/100KSPS, Serial SPI QSPI I/O, Iq=1uA in S'Down, 8 Pin uSOIC
AD7888	12 BIT, 200KSPS, Iq=0.7mA, <1uA sleep I, ref on-chip, auto pwr-down & stndby modes; +2.7V>+5.25V, Smallest 8 CH DAS 16-pin TSSOP.

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD7889	12 BIT, 600KSPS, +5V @18mA, Iq=75uA in STBY, +/-10V=-1, +/-2.5=-2, 0>=2.5=-3, Serial I/O, Self Clocking or Ext.
AD7890	12 BIT, 83 KSPS, 8 Channel MUX AND S/H, Various Input Ranges, +5V RAIL, Serial I/O
AD7891	12 BIT, 8 Channel MUX AND S/H, 454KSPS, Various Input Ranges, w/Parallel I/O, 300KSPS Serial I/O,+5V RAIL, 75mW, wSHUTDOWN
AD7892	12 BIT, 500 KSPS -1, -2, 600KSPS -3, 12 BIT Parallel or Serial I/O, +5V RAIL, $\pm 5$ or $\pm 10V$ INPUT -1, 0 to +2.5 -2, $\pm 2.5$ -3
AD7893	12 BIT, 117 KSPS, Serial I/O, +5V RAIL, 8 PIN SOIC/DIP
AD7894	14 BIT, 163KSPS, 4.5usec, +5V @ 4mA, THREE MODELS -10=+/-10V, -3=+/-2.5V, -2=+2.5V INPUT, AUTO POWER DOWN Iq=5uA, EXT Vref=+2.5V, Serial I/O, 8 pins, AD7895 Pin Compatible
AD7895	12 BIT, 200KSPS, +5V @ 10mA, THREE MODELS -10=+/-10V, -3=+/-2.5V, -2=+2.5V INPUT, AUTO POWER DOWN Iq=10uA, EXT Vref=+2.5V, Serial I/O, 8 pin
AD7896	12 BIT, 100KSPS, Serial I/O, AUTO POWER DOWN, +3V, Iq=5mA, Iq=7uA IN SHUTDOWN MODE, 8 PIN SOIC/DIP
AD790	SINGLE, WIDE CMV RANGE -Vs to +Vs-2V, 45 nsec, +5V or +/-15V RAIL
AD7943	SINGLE, 12 BIT, INVERTED R 2R, LOW POWER AD7543, 200 uA, +5V RAIL ONLY, FAST Serial I/O
AD7945	SINGLE, 12 BIT, INVERTED R 2R, LOW POWER AD7545A, 200 uA, +5V RAIL ONLY, FAST Parallel I/O
AD7948	SINGLE, 12 BIT, INVERTED R 2R, LOW POWER AD7548A, 200 uA, +5V RAIL ONLY, FAST 8 BIT I/O
AD795	SINGLE, LOW DRIFT JFET, Ib=1pA, Vos=1uV/C, Pin Replacement for Burr Brown OPA111
AD797	SINGLE, Vos=40 to 80uV, TCVos=1uV/C, eN=1.2nV/SQRTHz, EXTREMELY LOW DISTORTION/NOISE OVER 0 to 50KHz, Settles to 16 Bit Accuracy in 60nsec
AD800	44.736 MHz NRZ, or 52 MHZ, CLOCK RECOVERY, DATA RETIME, INTERNAL VCXO
AD8001	SINGLE, 650 MHz, 1500V/usec, Iq=5.5mA, $\pm 5V$ RAILS
AD8001R	EVALUATION BOARD, AD8001, \$37.50, PART INCLUDED
AD8002	DUAL, $\pm 5V$ , 600MHz, 1200Vusec, 5.5mA/AMP, 0.1dB FLAT TO 60MHz
AD8004	QUAD, $\pm 5V$ or +5V, Iq=3.9mA/AMP, 250MHz, 3000Vusec, THD=-78dBc @ 5MHz,0.1db FLAT TO 30MHz
AD8005	SINGLE, LOW POWER, 225MHz, 280V/usec, THD=-50dB @10MHz, 10Mhz to 0.1dB, 28nsec, Iout=10mA, +5V or +/-5V RAIL, LOW POWER, Iq=475uA

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD8009	SINGLE, 1GHz, 5500V/usec, FPBW=440MHz, THD=-60/48dBc @ 20/250MHz, SFDR=44dBc, I3P=18dBc @ 150MHz, Iout=75mA, Iq=11mA
AD8010	SINGLE, "VIDEO DISTRIBUTION AMP", can drive 8, 75 ohm back terminated cables, 150MHz, 1200V/usec, 0.1dB=30MHZ, -60dBc @ 20MHZ, +/-5V Rails, Iout=160mA
AD8011	SINGLE, 340MHz, 3500V/usec, +5V or +/-5V RAIL, LOW POWER, Iq=1mA
AD8012	DUAL, 350MHz, 1300V/usec, THD=-52dBc @ 5MHz, -60dBc @ 5MHz, +5V RAIL, Iout=25mA, Iq=1.8mA
AD8013	TRIPLE, +5V, Iq=2ma/amp, 150MHz, 1000V/usec, 0.1 dB to 50MHz, w/DISABLE, DRV 300pF Cap Laod, Iout=25mA
AD8015	SINGLE, FIBER OPTIC PREAMP, 200MHz, 1800V/usec, SINGLE SUPPLY Vs=+5V, LOW NOISE
AD802	155.52 MHz, CLOCK RECOVERY, DATA RETIME, INTERNAL VCXO
AD8023	TRIPLE, +5V, Iq=2.5ma/amp, 210MHz, 1000V/usec, 0.1 dB to 50MHz, w/DISABLE, DRV 300pF Cap Load, Iout=70mA
AD803	20.48 MHz, CLOCK RECOVERY, DATA RETIME, INTERNAL VCO, for PON's PASSIVE OPTICAL NETWORKS
AD8031	SINGLE, Av=1, RAIL to RAIL INPUT/OUTPUT, 80MHz, 15V/usec, 125nsec to 0.1%, Iq=750uA, +2.5>12V Rail, Iout=20mA
AD8032	DUAL, Av=2,RAIL to RAIL INPUT/OUTPUT, 80MHz, 30V/usec, 125nsec to 0.1%, Iq=1.8mA, +2.5>12V SUPPLY, Iout=20mA
AD8036	SINGLE, CLAMP AMP 3nsec clamp recovery, 150 MHz, Av=1, 1600v/sec, -64dBc,
AD8037	SINGLE, CLAMP AMP 3nsec clamp recovery, 200 MHz, Av=2, 1600v/sec, -64dBc,
AD8041	SINGLE, 160MHz, OUTPUT RAIL to RAIL, 25MHz FPWBW, 130V/usec, w/Disable, Vs=+3V > +/-5V Rail, Iq=5.5mA
AD8042	DUAL, 130MHz, RAIL to RAIL OUTPUT, 25MHz FPWBW, 130V/usec, Vs=+3V > +/-5V SUPPLY. Iq=5.5mA/AMP
AD8044	QUAD, 150MHz, RAIL to RAIL OUTPUT, SINGLE +3V > +/-5V SUPPLY, Iq=3mA/AMP
AD8047	SINGLE, AV=1, 170MHz Small Signal, 100MHZ Large signal, , 13nec to 0.1%, 2nd Harnonic=-54 dBc @ 20MHZ, 475V/usec, +/-5V, Iq=6.6mA
AD8048	SINGLE, AV=2, 180MHz Small Signal, 135MHZ Large signal, , 13nec to 0.1%, 2nd Harnonic=-48 dBc @ 20MHZ, 475V/usec, +/-5V, Iq=6.6mA
AD805	155.52 MHz, CLOCK RECOVERY, DATA RETIME, EXTERNAL VCXO, CCITT G.958 TYPE A or B
AD8051	SINGLE, 150MHz, OUTPUT RAIL to RAIL, 300V/usec, Vs=+3V > +/-5V SUPPLY, Iq=6mA, Iout=50mA

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD8052	DUAL, 150MHz, OUTPUT RAIL to RAIL, 350V/usec, Vs=+3V > +/-5V SUPPLY, Iq=12mA, Iout=50mA
AD8054	QUAD, 150MHz, OUTPUT RAIL to RAIL, 350V/usec, Vs=+3V > +/-5V SUPPLY, Iq=12mA, Iout=50mA
AD8055	SINGLE, 350MHz, 30MHZ 0.1dB, 1000V/usec, Diff phase/Gain=.02/.03 Vs=+/-5V Rails, Iq=5mA, Iout=50mA
AD8056	DUAL, 350MHz, 30MHZ 0.1dB, 1000V/usec, Diff phase/Gain=.02/.03 Vs=+/-5V Rails, Iq=5mA, Iout=50mA
AD807	CLOCK RECOVERY 155mhz, ON CHIP OSCILLATOR & COMPARATOR, PLL, 2deg. RMS JITTER, with data drop out indicator, CCITT G.958 TYPE A/BELLCORE TR-NWT-000253 for OC-3
AD8072	DUAL, 60 MHz, 400V/usec, 20 nsec to 0.1%, GAIN FLAT=10MHz, LOW COST, Iq=6mA, +5V or +/-5V
AD8073	TRIPLE, 60 MHz, 400V/usec, 20 nsec to 0.1%, GAIN FLAT=10MHz, LOW COST, Iq=9mA, +5V or +/-5V
AD8079	DUAL, 450MHZ, 0.1dB =60MHz, Crosstalk=-60dB, A=Av=2, -B=Av=2.2, Internal Laser Trimmed Resistors 0.1%
AD808	Fiber Optic Receiver IC with Quantizer, PLL, 2degrees RMS output jitter, and Clock Recovery (622 Mbps), OC-12, SDH STM-4
AD809	FREQUENCY SYNTHESIZER, 155 MBPS BIT CLOCK from 19.4 or 9.72MHz INPUT, I/O ECL/PECL/TTL/CMOS, +5V or -5.2V
AD810	SINGLE, 1000 V/usec, 100 MHz, 0.1dB @ 15MHz, w/DISABLE
AD8108	8X8 VIDEO CROSS POINT, BUFFERED IN/OUT, 325MHz, 0.1dB=40MHz, 400V/usec, Av=1, DIFF PHASE/GAIN .01/.01, Iq=33mA, +/-5V RAILS, Vout= +/-2.5V, Iout=20mA
AD8109	8X8 VIDEO CROSS POINT, BUFFERED IN/OUT, 200MHz, 0.1dB=40MHz, 480V/usec, Av=2, DIFF PHASE/GAIN .01/.01, Iq=33mA, +/-5V RAILS, Vout= +/-2.5V, Iout=20mA
AD811	SINGLE, 2500 V/usec, 120 MHz, 0.1dB @ 35MHz, Iout=100mA
AD8110	16X8 VIDEO CROSS POINT, BUFFERED IN/OUT, 200MHz, 0.1dB=40MHz, 800V/usec, Av=1, DIFF PHASE/GAIN .01/.01, Iq=80mA, +/-5V RAILS, Vout= +/-2.5V, Iout=20mA
AD8111	16X8 VIDEO CROSS POINT, BUFFERED IN/OUT, 200MHz, 0.1dB=40MHz, 800V/usec, Av=2, DIFF PHASE/GAIN .01/.01, Iq=80mA, +/-5V RAILS, Vout= +/-2.5V, Iout=20mA
AD8116	16X16 VIDEO CROSS POINT, BUFFERED IN/OUT, 200MHz, 0.1dB=40MHz, 800V/usec, Av=1, DIFF PHASE/GAIN .01/.01, Iq=80mA, +/-5V RAILS, Vout= +/-2.5V, Iout=20mA
AD812	DUAL, 145MHz, 1600V/usec, 0.1dB FLAT TO 40MHz, Iq=11mA, +5V or +/-15 Rails
AD813	TRIPLE, 80 MHz, 2500V/usec, w/THREE DISABLE PINS, +5 or +/-15V Rails

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD815	DUAL, DIFFERENTIAL 50MHz "H" TYPE DRIVER. +/-20V, ADSL LINE DRIVER, >500mA OUTPUT, THD -65 dB @ 1MHz
AD816	QUAD, ADSL TRANSCEIVER, DRIVER SIDE 26dBm, Diff Line Drv, 40Vpp into 50 ohms, THD=-66dB @ 1MHz, 120MHz, 900V/usec RECEIVER SIDE, 1100MHz, 200V/usec, Iout=70mA
AD817	SINGLE, Av=1, 50MHz, 350V/usec, +5V or +/-5V or +/-15V, Iout=50mA, Iq=7.5mA, STABLE WITH ANY CAP LOAD
AD8170	2 Channel BUFFERED, 250MHz, 0.1dB=85MHz, 1000V/usec, DIFF PHASE/GAIN .05/.02, 10nsec SWITCH, Iq=7.5mA, +/-5V RAILS, Vout= +/-3.8V, Iout=50mA
AD8174	4 Channel BUFFERED, 250MHz, 0.1dB=85MHz, 1000V/usec, DIFF PHASE/GAIN .05/.02, 10nsec SWITCH, Iq=7.5mA, +/-5V RAILS, Vout= +/-3.8V, Iout=50mA
AD818	SINGLE, Av=2, 130MHz, 500V/usec, Iout=50mA, LOW DISTORTION OUTPUT STAGE
AD8180	2 Channel, 750MHz, 0.1dB BW=100MHz, 750V/usec, tSWITCH=10nsec, Gain=1, Iout=30mA, w/Disable/CH
AD8182	DUAL, 2 Channel, 640MHz, 0.1dB BW=100MHz, 750V/usec, tSWITCH=10nsec, Gain=1, Iout=30mA, w/Disable/CH
AD8184	4 Channel, 640MHz, 0.1dB BW=100MHz, 750V/usec, tswitch=10nsec, Gain=1, Iout=30mA, w/Disable/CH
AD820	SINGLE, +3V, Iq=0.8mA, 2MHz, 1.2V/usec, Ib=10pA, RAIL TO RAIL OUTPUT, Iq=750uA, 20mA OUTPUT
AD822	DUAL, +3V, Iq=1.6mA, 2MHz, 1.2V/usec, Ib=10pA, RAIL TO RAIL OUTPUT, Iq=750uA, 20mA OUTPUT
AD823	DUAL, 16MHz, Vos=800uV, Ib=25pA, 26V/usec, +3 to +36V, RAIL TO RAIL
AD824	QUAD, +3V, Iq=3.2mA, 2MHz, 1.2V/usec, Ib=10pA, RAIL TO RAIL OUTPUT, Iq=750uA, 20mA OUTPUT
AD825	SINGLE, 50MHz, Vos=800uV, Ib=25pA, 140V/usec, +3 to +36V, RAIL TO RAIL
AD826	DUAL, Av=1, 50MHz, 350V/usec, +5V or +/-5V or +/-15V, STABLE WITH CAP LOAD
AD827	DUAL, Av=1, 50MHz, 350V/usec, +/-5V or +/-15V, Iout=20mA, STABLE WITH CAP LOAD
AD828	DUAL, Av=2, 130MHz, 500V/usec, Iout=50mA, LOW DISTORTION OUTPUT STAGE
AD829	SINGLE, 120MHz UNCOMPENSATED, 230V/usec, eN=1.7nV/SQRTHz, +/-5>15V Rails
AD830	SINGLE, DIFFERENTIAL VIDEO RECEIVER, 35MHz, 210V/usec
AD8300	SINGLE, 12 BIT, +2.7 to +5.5V SUPPLY, COMPLETE, 2.048V FS, 6usec settling, 5mW, Serial
AD8303	DUAL, 12 BIT, +2.7 to +5.5V SUPPLY, COMPLETE, 2.048V FS, 6usec settling, 10mW, Serial, w/SHUTDOWN

[Click on a part to see Datasheet.](#)

# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD8307	DC to 500 MHz, -74>+16dB, dB DEMODULATING LOG AMP , eN=1.5nV/sqrthz, Slope 25mV/dB, Intercept=80dB, w/o Limiter, +2.7V @ Iq=7.5mA, w/S'Down=Iq=10uA
AD8309	DC to 400 MHz, -74>+20dB, dB DEMODULATING LOG AMP & LIMITER, 80dB Limiter Sensitivity, 100psec Limiter Skew, , eN=1.0nV/sqrthz,B, +2.7V @ Iq=12mA
AD831	500 MHz RF&LO, +24dBm IP3, +10dBm COMPRESSION POINT, NF=10.3dB @ 70MHz, +5V
AD8320	SINGLE, 8 BIT DIGITALLY CONTROLLED ATTENUATOR, 200MHz, 36dB ATTEN. RANGE (+24dBto -12dB), Harmonic Distort @ 50MHz=-40dBc @+18dBm Output, -52 @10dBm, Serial SPI I/O, +5V@ 70mA, 3mA in s/down
AD834	ACCURACY=2%, 500MHz, DIFFERENTIAL CURRENT OUTPUT, Vout=4mA*(Vx*Vy/V^24 QUADRANT
AD835	ACCURACY=5%, 200MHz, VOLTAGE OUTPUT, PINS WITH AD834,Vout=(X1-X2)*(Y1-Y2)/U +Z, 4 QUADRANT
AD840	SINGLE,AD841 DECOMP'D 40MHz @Av=10, SR=350V/usec, 80nsec to 0.1%
AD8400	SINGLE, 8 BIT, DIGITALLY PROGRAMMABLE RESISTOR, LOOKS LIKE 3 TERMINAL POTENTIOMETER, 1K, 10K, 50K, 100K, +3/5V RAIL
AD8401	8 BIT A/D, 4 Channel MUX,+ T/H, w/ 8 BIT 1usec D/A: WAS DAS-09
AD8402	DUAL, 8 BIT, DIGITALLY PROGRAMMABLE RESISTORS, LOOKS LIKE 3 TERMINAL POTENTIOMETER, 1K, 10K, 50K, 100K, +3/5V RAIL
AD8403	QUAD, 8 BIT, DIGITALLY PROGRAMMABLE RESISTORS, LOOKS LIKE 3 TERMINAL POTENTIOMETER, 1K, 10K, 50K, 100K, +3/5V RAIL
AD841	SINGLE, 40 MHz @Av=1, 200V/usec, 90nsec to 0.1%, +/-15V Rails,
AD842	SINGLE, HIGH OUTPUT CURRENT=100mA, AD841 DECOMP'D 40MHz @Av=2, 300V/usec, 80nsec to 0.1%, +/-15V Rails,
AD843	SINGLE, 35 MHz, 300 V/usec, 95nsec to 0.1%, +/-15V Rails, Ib=2.5nA, NO THERMAL TAIL
AD844	SINGLE, 2000V/usec, Excellant DC Specs, Eos=150>300uV, Ib=250>450nA, 60 MHz, +/-15V Rails
AD845	SINGLE, 16 MH, 50V/usec, Ib=2nA, Eos=250>1000uV, 250nsec to 0.1%, +/-15V Rails
AD846	SINGLE, 80 MHz, 450V/usec, GOOD DC ACCURACY Eos=300uV, Ib=450nA, +/-15V Rails
AD847	SINGLE, Av=1, 50MHz, 350V/usec, +/-5V or +/-15V, Iout=20mA, STABLE WITH CAP LOAD, See AD817 for new designs
AD848	SINGLE, Av=2, 50MHz, 350V/usec, +/-5V or +/-15V, Iout=20mA, STABLE WITH CAP LOAD, See AD826 for new designs

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD849	SINGLE, $A_v=25$ , $GWB=725\text{MHz}$ , $350\text{V}/\mu\text{sec}$
AD8509	9 Channel LCD DRIVERS, 2 Muxed Inputs/Channel, $V_{os}=20\text{mV}$ , $I_b=\text{tbd}$ , $V_{oh}=4.9\text{V}$ , $V_{ol}=100\text{mV}$ , Slew Rate= $5\text{V}/\mu\text{sec}$ , $+3.3\text{V}$ @ $6\text{mA}$
AD8511	11Channel LCD DRIVERS, 2 Muxed Inputs/Channel, $V_{os}=20\text{mV}$ , $I_b=\text{tbd}$ , $V_{oh}=4.9\text{V}$ , $V_{ol}=100\text{mV}$ , Slew Rate= $5\text{V}/\mu\text{sec}$ , $+3.3\text{V}$ @ $9\text{mA}$
AD8519	SINGLE, $15\text{MHz}$ , $+3>12\text{V}$ , $SR=4\text{V}/\mu\text{sec}$ , $E_{os}=1.1\text{mV}$ , $I_b=800\text{nA}$ , $I_{out}=25\text{mA}$ , Rail to Rail Out, SOT23-5
AD8522	DUAL, 12 BIT, $+5\text{V}$ RAIL, COMPLETE, Serial I/O, 14 PINS
AD8531	SINGLE, CMOS, $900\text{ }\mu\text{A}/\text{AMP}$ , RAIL TO RAIL IN/OUT, $+3\text{V}$ to $+6\text{V}$ , $2\text{MHz}$ , $3.4\text{V}/\mu\text{sec}$ , $V_{os}=25\text{mV}$ , $I_b=25\text{pA}$ , $I_{out}=250\text{mA}$ , not short circuit proof
AD8532	DUAL, CMOS, $900\text{ }\mu\text{A}/\text{AMP}$ , RAIL TO RAIL IN/OUT, $+3\text{V}$ to $+6\text{V}$ , $2\text{MHz}$ , $3.4\text{V}/\mu\text{sec}$ , $V_{os}=25\text{mV}$ , $I_b=25\text{pA}$ , $I_{out}=250\text{mA}$ , not short circuit proof
AD8534	QUAD, CMOS, LOW POWER $900\text{ }\mu\text{A}/\text{AMP}$ , RAIL TO RAIL IN/OUT, $+3\text{V}$ to $+6\text{V}$ RAIL to RAIL IN/OUT, $2\text{MHz}$ , $3.4\text{V}/\mu\text{sec}$ , $V_{os}=25\text{mV}$ , $I_b=25\text{pA}$ , $I_{out}=250\text{mA}$
AD8541	SINGLE, JFET, LOW POWER $I_q=50\text{ }\mu\text{A}$ , RAIL TO RAIL IN/OUT, $+3\text{V}$ to $+5.5\text{V}$ RAIL to RAIL IN/OUT, $BW=500\text{ KHz}$ , $SLEW=0.8\text{V}/\mu\text{sec}$ , $E_{os}=5\text{mV}$ , $I_b=4\text{pA}$
AD8542	DUAL, JFET, LOW POWER $I_q=100\text{ }\mu\text{A}$ , RAIL TO RAIL IN/OUT, $+3\text{V}$ to $+5.5\text{V}$ RAIL to RAIL IN/OUT, $BW=500\text{ KHz}$ , $SLEW=0.8\text{V}/\mu\text{sec}$ , $E_{os}=5\text{mV}$ , $I_b=4\text{pA}$
AD8544	QUAD, JFET, LOW POWER $I_q=100\text{ }\mu\text{A}$ , RAIL TO RAIL IN/OUT, $+3\text{V}$ to $+5.5\text{V}$ RAIL to RAIL IN/OUT, $BW=500\text{ KHz}$ , $SLEW=0.8\text{V}/\mu\text{sec}$ , $E_{os}=5\text{mV}$ , $I_b=4\text{pA}$
AD8551	SINGLE, CHOPPER, $E_{os}=5\text{uV}$ , $TCV_{os}=0.05\text{uV}/\text{C}$ , $I_b=50\text{pA}$ , $+2.7\text{V}$ @ $600\text{uA}$ , $1.5\text{Mhz}$ , $0.8\text{V}/\text{sec}$ , R to R In & Out
AD8552	DUAL, CHOPPER, $E_{os}=5\text{uV}$ , $TCV_{os}=0.05\text{uV}/\text{C}$ , $I_b=50\text{pA}$ , $+3\text{V}$ @ $1.2\text{mA}$ , $1.5\text{Mhz}$ , $0.8\text{V}/\text{sec}$ , R to R In & Out
AD8554	QUAD, CHOPPER, $E_{os}=5\text{uV}$ , $TCV_{os}=0.05\text{uV}/\text{C}$ , $I_b=50\text{pA}$ , $+3\text{V}$ @ $1.2\text{mA}$ , $1.5\text{Mhz}$ , $0.8\text{V}/\text{sec}$ , R to R In & Out
AD8561	SINGLE, $+3\text{V}$ or $\pm 5\text{V}$ , PROP DELAY $8\text{nsec}$ , no latch, $I_q=6\text{mA}$ , $V_{cm}=0>3.2\text{V}$ , (was CMP604)
AD8564	QUAD, $+3\text{V}$ or $\pm 5\text{V}$ , PROP DELAY $8\text{nsec}$ , no latch, $I_q=24\text{mA}$ , $V_{cm}=0>3.2\text{V}$ , (was CMP604)
AD8582	DUAL, 12 BIT, $+5\text{V}$ RAIL, COMPLETE, Parallel I/O, 20 PINS
AD8598	DUAL, $+3\text{V}$ or $\pm 5\text{V}$ , PROP DELAY $8\text{nsec}$ , no latch, $I_q=24\text{mA}$ , $V_{cm}=0>3.2\text{V}$ , (was CMP604)
AD8600	HEXIDECIMAL, 8 BIT, 2QM MULTIPLYING, Parallel I/O, $2\text{ }\mu\text{sec}$ , $175\text{mW}$ , READBACK, $+5\text{V}$ @ $35\text{mA}$

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD871	12 BIT, 5 MSPS, Low Transistion Noise 0.3LSB RMS, +/-5V Rails, PINS WITH AD872
AD872A	12 BIT, 10 MSPS, +/- 5V Rails, 1 WATT, IMPROVED AD872, LOWER RMS NOISE
AD876	8, or 10 BIT Versions, 20 MSPS, WITH T/H, SINGLE RAIL +5V, LOW POWER 165 mW.
AD8801	OCTAL, 8 BIT, UNBUFFERED VOLTAGE OUTPUT, Rout= 10K, +Vref, RESET to MIDSCALE
AD8802	12 Channel, 8 BIT, UNBUFFERED VOLTAGE OUTPUT, Rout= 10K, +Vref, RESET to MIDSCALE, w/SHUTDOWN
AD8803	OCTAL, 8 BIT, UNBUFFERED VOLTAGE OUTPUT, Rout= 10K, +Vref and -Vref, for span and offset
AD8804	12 Channel, 8 BIT, UNBUFFERED VOLTAGE OUTPUT, Rout= 10K, +Vref and -Vref, for span and offset, w/SHUTDOWN
AD8842	OCTAL, 8 BIT, LOWPOWER (90mW) VERSION OF DAC8840, 10KHZ 4QM BW
AD9000	6 BIT, 50>75 MSPS, -5.2/+5V Rails, Iq=75mA, Input FPBW=20MHZ, Vref BW=20MHZ
AD9002	8 BIT, 125 MSPS, -5.2V Rail, Iq=175mA, Input FPBW=160MHZ, Vref BW=10MHZ
AD9003	12 BIT, 1 MSPS, +/-15 @+5V Rails, 3.2watts, @ 1MHz SNR=65dB, See AD1671
AD9005B	12 BIT, 10 MSPS , See AD9220, AD9200
AD9007	12 BIT, 10 MSPS , +/-5V @3.9Watts, @ 4.3MHz THD=-70dB, SNR=63dB, HIGH DYNAMIC RANGE, See AD9220, AD9200
AD9012	8 BIT, 75 MSPS, -5.2/+5V Rails, Iq=179/45mA, Input FPBW=160MHZ, Vref BW=10MHZ
AD9014	14 BIT, 1 MSPS, +/-15V Rails, Iq=245/130mA, Input FPBW=160MHZ, Vref BW=10MHZ
AD9020	10 BIT, 60 MSPS, SNR=51dB, SINAD=50dB @ Fin=10.3MHz, Vs= +/-5V @ 530/170mA
AD9034	12 BIT, DC to 20MSPS, HYBRID, SAMPLE RATE TO DC, See AD9042
AD9040A	10 BIT, 40 MSPS, (SNR=52dB, SINAD=51dB @ Fin=2.3MHz), Vs= +/-5V @ 100mA
AD9042	12BIT, 41MSPS, SNR=70dB, SFDR=69dB @ Fin=9.6MHz, +5V RAIL, 575mW
AD9048	8 BIT, 35MSPS, SINAD=45dB @ Fin 1.248MHz, Vs= +/-5V
AD9049	9 BIT, 30MSPS, Analog BW=100MHZ, 53.3 ENOB 2Fin=2.3MHz, +5V @ 80mA, +2.5Vref on chip, selectable 3 or 5V Logic
AD9049P	EVALUATION BOARD FOR THE AD9049

[Click on a part to see Datasheet.](#)



## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD9050	10 BIT, 40/60 MSPS, SINAD=55dB @Fin 10.3MHz, TTL, Vs=+5V @58mA, 290mW, LOW POWER VERSION OF AD9040A
AD9050P	EVALUATION BOARD FOR AD9050, \$275, PART INCLUDED
AD9051	10 BIT, 60 MSPS, SINAD=58dB @Fin 10.3MHz, BW=120Mhz @ 1Vpp, 40Mhz 2Vpp, Vs=+5V @58mA
AD9054	8 BIT, Dash-135MSPS, Dash-200MSPS, Analog BW=350MHz, 7.35 ENOB @ Fin=10.3MHz (SINAD=46dB), .6.7 ENOB @ Fin=76MHz (SINAD=42dB),+5V @60mA, DEMUXED or SINGLE OUTPUT
AD9057	8 BIT, 40/60 /80MSPS, INL&DNL Over Temp=2Lsb's, 7.1 ENOB @ Fin=10.3MHz (SINAD=44.5dB), +3/5V @ 68mA, DIGITAL TRI-STATE OUPUTS, w/Power Down, ANALOG BW=120MHz, SINGLE CLOCK
AD9058	8 BIT, DUAL,50 MSPS, +/- 5V RAILS, 2 CLOCKS , CAN BE INTERLEAVED
AD9059	8 BIT, DUAL, 60 MSPS, 7.1 ENOB @ Fin=10.3MHz (SINAD=44.5dB), +3/5V @ 80mA, DIGITAL TRI-STATE OUPUTS, w/Power Down, ANALOG BW=120MHz, SINGLE CLOCK
AD9060	10 BIT, 75 MSPS, JTHD=55dB, SNR=51dB, SINAD=50dB @ Fin=10.3MHz,) Vs=+/-5.2V @ 500/180mA
AD9066	6 BIT, DUAL, 60MSPS, +5V @ 80mA, INTERNAL Vref,
AD9070	10 BIT, 100MSPS, 9 ENOBS @ 10.3MHz (sinad=56dB), -5V @ 140mA, -2.5Vref on chip, PINS w/AD9050, ECL LOGIC, or in the +5V Positive ECL Mode
AD9071	10 BIT, 100MSPS, 8.7 ENOBS @ 10.3MHz (54dB), +5V @ 140mA, +2.5Vref on chip, PINS w/AD9050, TTL LOGIC
AD9100	SINGLE, 150MHz, 23nsec to .01%, 14 BIT ACCURATE, +/-1V INPUT RANGE
AD9101	SINGLE, 160MHz, 7nsec to 0.1%, 12 BIT ACCURATE, +/-1V INPUT RANGE
AD9200	10 BIT, 20 MSPS, TTL, +2.7 to 5.5V Rail, w/INPUT CLAMP & OUT of RANGE INDICATOR, POWERDOWN, Three State outputs, pins with AD876
AD9201	DUAL, 10 BIT, 20MSPS, +2.7V to 5.5V @ 35mA, +1.0Vref on chip, SNR=58dB, SFDR=-68dB, w/ INPUT BUFFER
AD9220	12 BIT, 10 MSPS, +5V RAIL, 370mW, SFDR=83dB @ Fin=1.2MHz
AD9221	12 BIT, 1 MSPS, +5V RAIL, 50mW, SFDR=75dB @ Fin=1.2MHz
AD9223	12 BIT, 3 MSPS, +5V RAIL, 100mW, SFDR=75dB @ Fin=1.2MHz
AD9224	12-BIT, 40MSPS, +5V, 400mW, SFDR 73dB
AD9225	12 BIT, 25 MSPS, SNR=70dB, SFDR=80dB, +5V RAIL, Out of Range Indicator, 350mW, Pins with AD9220

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD9240	14 BIT, 10 MSPS, @Fin=5MHz, ENOBS=12.2, SINAD=75dB, SNR=78.5, THD=-77dB, SFDR=80dB, +5V @ 65mA
AD9241	14 BIT, 1 MSPS, .085 LSB DNL, 2.5 INL, SNR=-77db @ 250KHZ, SFDR=-84 dB @ 250KHZ, +5V @ 20mA
AD9243	14 BIT, 3 MSPS, .085 LSB DNL, SNR=-77db @ 250KHZ, SFDR=-84 dB @ 250KHZ, +5V @ 20mA
AD9260	16 BIT, 1.2MHz, MULTI-BIT SELECTABLE SIGMA DELTA 1/2/4/8X VARIABLE OVER SAMPLE RATE, 125KHZ>20MHz, +5V @ 30 to 100mA, 82dB Stopband Aten, 0.01% Bandpass Ripple
AD9280	8 BIT, SINGLE, 32MSPS, +2.7V to 5.5V @ 35mA, +1.0Vref on chip, SNR=48dB, SFDR=-56dB, w/ INPUT BUFFER
AD9281	8 BIT, DUAL, 32MSPS, +2.7V to 5.5V @ 35mA, +1.0Vref on chip, SNR=48dB, SFDR=-56dB, w/ INPUT BUFFER
AD9300	4 to 1 MUX, 34MHz, 0.1dB FLAT TO 8MHz, 0.03/0.01/DIFF PHASE GAIN, See AD8177/8184 for new designs
AD9500	DELAY GENERATOR, 2.5 nsec to 10 usec, Max Trigger Rate=60MHz, ±5V SUPPLIES, w/SERARATE RESET & TRIGGER, See AD9501
AD9501	DELAY GENERATOR, 2.5 nsec to 10 usec, Max Ttrigger Rate=22MHz, +5V SUPPLY
AD9560A	8 BITS, 0.5 to 50 MHz, VIDEO SPEED PULSE WIDTH MODULATOR
AD9561	8 BITS, 0.5 to 50 MHz, VIDEO SPEED PULSE WIDTH MODULATOR, LOW COST
AD9610	SINGLE, 80MHz, SLEW RATE=3000V/usec, Vs=+/-15V @27mA, HYBRID AMPLIFIER
AD9617	SINGLE, Av=1, 130MHz, 0.6dB FLAT to 50MHz, LOW DISTORTION 2nd/3rd @20MHz Rload 500ohm=-55dBc, Iq=43mA
AD9618	SINGLE, Av=5, 130MHz, 0.6dB FLAT to 50MHz, LOW DISTORTION, 2nd/3rd @20MHz Rload 500ohm=-55dBc, SLEW RATE=1400V/usec, Iq=43mA
AD9630	750 MHz CLOSED LOOP BUFFER, +/-5V RAILS
AD9631	SINGLE, Av=1, 220MHz, 0.1dB FLAT to 150MHz, 2nd/3rd @20MHz Rload 500ohm=-65dBc, 1000V/usec, Iq=17mA
AD9632	SINGLE, Av=2, 180MHz, 0.1dB FLAT to 155MHz, 2nd/3rd @20MHz Rload 500ohm=-65dBc, 1000V/usec, Iq=17mA
AD9660	LASER DIODE DRIVER w/ LIGHT POWER CONTROL, 200MHz PULSE RATE, 2nsec Rise/Fall Time, 150mA Current Source for P Type diodes, 2 CALIBRATED POWER LEVELS, for MAGNETO OPTICAL DRIVES, LOW COST
AD9661A	LASER DIODE DRIVER w/ LIGHT POWER CONTROL, 200MHz PULSE RATE, 2nsec Rise/Fall Time, 120mA Crrrent Sink output for N Type diode, 1 CALIBRATED POWER LEVELS, for LASER BEAM PRINTERS
AD96685	SINGLE AD96685, PROP DELAY=3.5nsec, Output=Diff ECL, Vs=+/-5V @ 9/18mA

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD96687	DUAL AD96685, PROP DELAY=3.5nsec, Output=Diff ECL, Vs=+/-5V @ 9/18mA
AD9701	SINGLE, 8 BIT, 225MSPS, COMPLETE COMPOSITE FUNCTIONS, 8nsec, Vs=-5.2 @160mA
AD9708	SINGLE, 8 BIT, 100MSPS, Settling Time=35nsec, Glitch=5pVsec, +3/5V, 175mW, SFDR=-52dBc @ 20MHz, int Vref=+1.25V
AD9712B	SINGLE, 12 BIT DDS DAC, 100 MSPS, Vs=-5.2 @184mA, SFDR=68dBc @fIN=16MHz FS=40MHz 10MHz Span, TTL LOGIC
AD9713B	SINGLE, 10 BIT DDS DAC, 80 MSPS, Vs=+5/-5.2V @12/184mA, SFDR=68dBc @fIN=16MHz FS=40MHz 10MHz Span, TTL LOGIC
AD9731	SINGLE, 10 BIT, 170MSPS, TTL, SFDR=65dB @ 40MHz, +5V @55mA, ON CHIP Vref, w/SHUTDOWN, PINS w/AD9721
AD974	16 BIT, 4 CH, 100 & 200KHZ, +5V, Serial I/O, w/SHUTDOWN
AD976	16 BIT, 100KHZ, +5V, Iq=20mA, 8/16 BIT I/O, w/On chip Clk,
AD9760	SINGLE, 10 BIT, 100MSPS, Settling Time=35 nsec, Glitch=5nsec, +5V @ Iq=35mA Iout=20mA, SFDR=-55dBc @ 20MHz, int Vref=+1.25V, Pins with AD9760/62/64, Dash-50 Model=50MHz
AD9761	DUAL, 10 BIT, w/2x Interpolation Filter, 40MSPS, Settling Time=35 nsec, Glitch=5pVsec, +2.7/5.5V, 175mW, THD=58dB, SFDR 60dBc @ 15MHz, Int Vref=+1.25V
AD9762	SINGLE, 12 BIT, w/2x Interpolation Filter, 100MSPS, Settling Time=35 nsec, Glitch=5pVsec, Vs= +2.7/5.5V, 175mW w/p'Down, SFDR=-67dBc @ 20MHz, Int Vref=+1.25V, Pins with AD9760/62/64
AD9764	SINGLE, 14 BIT, 100MSPS, Settling Time=15 nsec, Glitch=5pVsec, +5V, 250mW w/P'Down=30mW, SFDR=-70dBc @ 5MHz, Int Vref=+1.25V, Pins with AD9760/62/64
AD9768	SINGLE, 8 BIT, 100 MSPS, SLEW RATE=400V/usec, SETTILING TIME=5nsec, GLITCH=200pV=sec, Iout=2>20mA, Vs=+/-5V @14/88mA
AD976A	16 BIT, 200KHZ, +5V & 10mA, Iq=20mA, 8/16 BIT I/O, w/On chip Clk,
AD977	16 BIT, 100KHZ, +5V & 20mA, Serial I/O, w/On chip Clk, w/SHUTDOWN
AD9774	SINGLE, 14 BIT, 128MHZ w/4x Interpolation Filter, 32MSPS Input, Settling Time=15 nsec, Glitch=5pVsec, +2.7>+5.5V, 500mW w/P'Down=30mW, SFDR=-72dBc @ 15MHz, Int Vref=+1.25V, Selectable sin(x)/x filter
AD977A	16 BIT, 200KHZ, +5V & 20mA, Serial I/O, w/On chip Clk, w/SHUTDOWN
AD9801	10 BIT 18 MSPS CCD PROCESSOR FOR STILL CAMERAS, w/PGA, CORRELATED DOUBLE SAMPLER, AGC, DC RESTORE, 185mW, not open market part

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
AD9802	10 BIT 18 MSPS CCD PROCESSOR FOR STILL CAMERAS, w/PGA, CORRELATED DOUBLE SAMPLER, AGC, DC RESTORE, 185mW, not open market part
AD9805	10 BIT 6 MSPS, wTRIPLE CORRELATED DOUBLE SAMPLER, 1X4 PGA, w/DIGITAL ADDER & MULTIPLIER FOR FOR OFFSET & GAIN ADJ.
AD9807	12 BIT 6 MSPS, wTRIPLE CORRELATED DOUBLE SAMPLER, 1X4 PGA, w/DIGITAL ADDER & MULTIPLIER FOR FOR OFFSET & GAIN ADJ. FRO SCANNERS
AD9830	32 BIT DDS, CLK=125MHZ, Fout=50MHz, +5V, Iq=25mA, CONTAINS NCO+SINE LOOK UP TABLE, FREQUENCY AND PHASE MODULATORS, PARALLAL I/O, 10 BIT D/A
AD9831	32 BIT DDS, CLK=20MHz, Fout=7MHz, +3.3V or+5V, Iq=15mA, CONTAINS NCO+SINE LOOK UP TABLE, FREQUENCY AND PHASE MODULATORS, PARALLAL I/O, 10 BIT D/A
AD9832	32 BIT DDS, CLK=25MHZ, Fout=8MHz, SFDR=72dB Narrow Band, +3.3/+5V, Iq=30mA w/P'Down, CONTAINS NCO+SINE LOOK UP TABLE, FREQUENCY AND PHASE MODULATORS, Serial 3 Pin I/O, 10 BIT D/A
AD9850	32 BIT DDS, 125MSPS, 5 BIT PHASE MODULATION, +5V 210mW, 42MHz CLOCK GENERATOR, 10 BIT D/A, ON CHIP COMPARATOR, 8 BIT I/O
AD9853	PROGRAMMABLE DIGITAL MODULATOR (FSK, QPSK, DQPSK, PI/4 or 16QAM), w/Raised Cosine Filer, +3V @ 70mA, 32 BIT TUNING WORD, FIR FILTERS, Fout=5>42MHz, SFDR=60dB @ 5MHz, 46dB @ 40MHz, PLL for 6X Ref CLK, 10 BIT D/A w/INVERSE SYNC FILTER
ADADC71	16 BIT, 14 bit accurate, 35usec, 0 to 70C, Parallel or Serial I/O, See AD676 for new designs
ADADC72	16 BIT, 14 bit accurate, 35usec, -25 to 85C, Parallel or Serial I/O, See AD676 for new designs
ADADC80	12 BIT, 0 to 70C, See AD1674 for new designs
ADADC84	12 BIT, 0 to 70C, See AD1674 for new designs
ADADC85	12 BIT, -25 to +85C, See AD1674 for new designs
ADC912A	12 BIT, 10usec, Low Transistion Noise= +/-1/6 of a Bit, 8/16 I/O, PINS WITH AD7572, no Int Vref.
ADDAC80	SINGLE, 12 BIT, Dash=I-CURRENT OUTPUT 300nsec., dash-v=Voltage Output 5usec, See AD566A for new new designs
ADDAC85	SINGLE, 12 BIT, -25 to +85C, Z GRADE +/-12V Rail, all others +/-15V, See AD667 or AD767 for new designs
ADDAC87	SINGLE, 12 BIT, -25 to +85C and -55 to +125C grades, Z GRADE +/-12V Rail, all others +/-15V, See AD667 or AD767 for new designs

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADDC02803	18 TO 28V INPUT, +3V OUTPUT AT 20 AMPS, INTEGRAL EMI FILTERS
ADDC02805	18 TO 28V INPUT, +5V OUTPUT AT 20 AMPS, INTEGRAL EMI FILTERS
ADDC02808	18 TO 28V INPUT, PULSED +8V OUTPUT AT 25 AMPS, INTEGRAL EMI FILTERS
ADDC02812	18 TO 28V INPUT, +/- 12V OUTPUT AT 8.33 AMPS, INTEGRAL EMI FILTERS
ADDC02815	18 TO 28V INPUT, +/- 15V OUTPUT AT 8.33 AMPS, INTEGRAL EMI FILTERS
ADDC27005	160 TO 400V INPUT, +270V NOMINAL INPUT, +5V OUTPUT AT 20 AMPS, INTEGRAL EMI FILTERS
ADDC27008	160 TO 400V INPUT, +270V NOMINAL INPUT, +8V OUTPUT AT 6.66 AMPS, INTEGRAL EMI FILTERS
ADDC27012	160 TO 400V INPUT, +270V NOMINAL INPUT, +/- 12V OUTPUT AT 8.33 AMPS, INTEGRAL EMI FILTERS
ADDC27015	160 TO 400V INPUT, +270V NOMINAL INPUT, +15V OUTPUT AT 7 AMPS, INTEGRAL EMI FILTERS
ADDG815	PERSONAL SOUND/COMM SYSTEM, PARTS, SCHEMATICS, BOARD, SOFTWARE
ADDS2101-3V	ADAPTOR FOR THE EZ-ICE, CONVERTS FROM +5V to +3.3V
ADDS2101EZI	LOW COST IN CIRCUIT EMULATOR FOR, AD2101,2103,2104,2105,2115, & 216x family
ADDS2101EZK	DEVELOPMENT KIT (21xx SWPC + AD01 EZ-LAB), FOR AD2101,2103,2104,2105,2115, & 216x family
ADDS2101EZL	DEVELOPMENT BOARD FOR, AD2101,2103,2104,2105,2115, & 216x family
ADDS21020EZ	JTAG, LOW COST IN CIRCUIT EMULATOR
ADDS21020EZ	EZ KIT AND DEVELOPMENT SOFTWARE, UNIVERSITY PROGRAM
ADDS2106X-E	EVALUATION BOARD FOR ADSP21062
ADDS2106XEE	SHARC EZ-KIT LITE (EUROPEAN VERSION)
ADDS2106XEZ	JTAG, LOW COST IN CIRCUIT EMULATOR, FOR AD21062
ADDS2106XEZ	EVALUATION BOARD FOR ADSP21062, COMES WITH ADDS-MAFE-1847
ADDS2106XEZ	SHARC EZ-KIT LITE
ADDS2106XIC	ADSP-2106X Embeddable ICE PC daught.card
ADDS210XXS	ADSP-21020 SOFTWARE, SIMULATOR + ASSEMBLER + C-COMPILER, ON IBM-PC

[Click on a part to see Datasheet.](#)

# ANALOG DEVICES CD REFERENCE

## PART DESCRIPTION

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ADDS210XXS	ADSP-21020 SOFTWARE, SIMULATOR + ASSEMBLER + C-COMPILER, ON SUN
ADDS210XXW	THREE DAY TRAINING COURSE, FLOATING POINT
ADDS2111EZI	LOW COST IN CIRCUIT EMULATOR, FOR ADSP2111
ADDS2111EZK	EVALUATION BOARD AND SOFTWARE FOR ADSP-2111, (21xx SWPC +2111 EZ-LAB)
ADDS2111EZL	EVALUATION BOARD FOR ADSP2111
ADDS2171EZI	IN CIRCUIT EMULATOR FOR ADSP2171/2173
ADDS2171EZI	IN CIRCUIT EMULATOR FOR ADSP2171, WITH PQFP CLIP ON ADAPTOR
ADDS2171EZL	EVALUATION BOARD FOR ADSP2171/2173
ADDS2171PQ	PQFP ADAPTOR/2171-EZ-ICE
ADDS218XEE	ADSP-218X FAMILY EMULATOR FOR EURO MKT
ADDS218XEZI	ADSP-218X SERIES INCIRCUIT EMULATR 40MHz
ADDS21XXEZL	EVALUATION KIT, ADSP2181, 33MIPS, + AD1847, + RS232 INTERFACE, +SOCKETED EPROM, SOFTWARE & SIMULATOR
ADDS21XXSW	ASSEMBLER/LINK/SIMULATOR /C-COMPILER and PSA SDK for ADSP-2100/01/05/15/MSP50, PC
ADDS21XXSW	ASSEMBLER/LINK/SIMULATOR /C-COMPILER and PSA SDK for ADSP-2100/01/05/15/MSP50, SUN
ADDS21XXWK	THREE DAY TRAINING COURSE, ADSP2100 FAMILY, FIXED POINT
ADEL2020	SINGLE, 500 V/usec, 90MHz, 0.1dB @ 25MHz, AD810 SPEC'D LIKE EL2020, w/DISABLE PIN
ADG201A	QUAD, SPST, 50ohm, LOGIC 1=SWITCH OFF, Ton=300nsec
ADG201HS	QUAD, SPST, 50ohm, FASTER ADG201, Ton=75nsec
ADG202A	QUAD, SPST, 50ohm, LOGIC 1=SWITCH ON, Ton=300nsec
ADG211A	QUAD, SPST, 115 ohm, ADG201 WITH HIGHER Ron, See ADG431 for new designs
ADG212A	QUAD, SPST, 115 ohm, ADG202 WITH HIGHER Ron, See ADG432 for new designs
ADG221	QUAD, SPST, 145 ohm, ADG201 WITH LATCH
ADG222	QUAD, SPST, 145 ohm, ADG202 WITH LATCH

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[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADG333A	QUAD, SPST, 35 ohm, +12 or +/-15V RAIL
ADG406	16 Channel, 100 ohm, SINGLE ENDED, NOT LATCHED, UPGRADE TO ADG506A
ADG407	8 Channel, 100 ohm, DIFFERENTIAL, NOT LATCHED, UPGRADE TO ADG507A
ADG408	8 Channel, 100 ohm, SINGLE ENDED, UP GRADE TO ADG508A, NOT LATCHED
ADG409	4 Channel, 100 ohm, DIFFERENTIAL, UPGRADE TO ADG509A, NOT LATCHED
ADG411	QUAD, SPST, 35 ohm, Vlogic=+5V, LOGIC 1=OFF, Ton=110nsec
ADG412	QUAD, SPST, 35 ohm, Vlogic=+5V, LOGIC 1=ON, Ton=110nsec
ADG413	QUAD, SPST, 35 ohm, Vlogic=+5V, LOGIC 1=2 ON and 2 OFF, Ton=110nsec
ADG417	SINGLE, SPST, 35 ohm, LOGIC 1=OFF, Ton=100nsec
ADG419	SINGLE, SPDT, 35 ohm, LOGIC 1= 1 ON and 1 OFF, Ton=100nsec
ADG426	16 Channel, 100 ohm, SINGLE ENDED, LATCHED
ADG428	8 Channel, 100 ohm, SINGLE ENDED, LATCHED, UPGRADE TO ADG528
ADG429	4 Channel, 100 ohm, DIFFERENTIAL LATCHED, UPGRADE TO ADG529
ADG431	QUAD, SPST, 24 ohm, Vlogic=+5V, LOGIC 1=OFF, Ton=60nsec
ADG432	QUAD, SPST, 24 ohm, Vlogic=+5V, LOGIC 1=ON, Ton=60nsec
ADG433	QUAD, SPST, 24 ohm, Vlogic=+5V, LOGIC 1=2 ON and 2 OFF, Ton=60nsec
ADG436	DUAL, SPDT, 10 ohm, 150nsec ton/toff, +/-3>20V
ADG438F	8 Channel, 300 ohm, SINGLE ENDED, NOT LATCHED, OVER VOLTAGE PROTECTED +40/-20V OVER THE RAIL
ADG439F	4 Channel, 300 ohm, DIFFERENTIAL, NOT LATCHED, OVER VOLTAGE PROTECTED +40/-20V OVER THE RAIL
ADG441	QUAD, SPST, 80ohm, No Vlogic, LOGIC 1=OFF, Ton=110nsec
ADG442	QUAD, SPST, 80 ohm, No Vlogic, LOGIC 1=ON, Ton=110nsec
ADG444	QUAD, SPST, 80 ohm, Vlogic=+5V, LOGIC 1=2 ON and 2 OFF, Ton=110nsec
ADG451	QUAD, SPST, 10 ohm, 44V, Vlogic=+5V, LOGIC 1=OFF, Ton=110nsec

[Click on a part to see Datasheet.](#)



## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADG452	QUAD, SPST, 10 ohm, 44V, Vlogic=+5V, LOGIC 1=ON, Ton=110nsec
ADG453	QUAD, SPST, 10 ohm, 44V, Vlogic=+5V, LOGIC 1=2 ON and 2 OFF, Ton=110nsec
ADG465	SINGLE, CH PROTECTOR, Ron=50 Ohm, over Voltage to +/-35V, Signal Path Open w/Power Off
ADG466	TRIPLE, CH PROTECTOR, Ron=80 Ohm, Over Voltage to +/-35V Vdd=Vss=0V, +/-20V Vdd=Vss=15V, Signal Path Open w/Power Off, Iq=8uA,
ADG467	OCTAL,CH ROTECTOR, Ron=95 Ohm, Over Voltage to +/-35V Vdd=Vss=0V, +/-20V Vdd=Vss=15V, Signal Path Open w/Power Off, Iq=8uA,
ADG506A	16 Channel, 300 ohm, SINGLE ENDED, NOT LATCHED, See ADG406 for new designs
ADG507A	8 Channel, 300 ohm, DIFFERENTIAL, NOT LATCHED, See ADG407 for new designs
ADG508A	8 Channel, 300 ohm, SINGLE ENDED, NOT LATCHED, See ADG408 for new designs
ADG508F	8 Channel, 300 ohm, SINGLE ENDED, NOT LATCHED, OVER VOLTAGE PROTECTED +/-20V OVER THE RAIL
ADG509A	4 Channel, 300 ohm, DIFFERENTIAL, NOT LATCHED, See ADG409 for new designs
ADG509F	4 Channel, 300 ohm, DIFFERENTIAL, NOT LATCHED, OVER VOLTAGE PROTECTED +/-20V OVER THE RAIL
ADG511	QUAD, SPST, 30 ohm, +3V or ±5V RAIL, .5uWATT, POSITIVE TRUE, Ton=200nsec
ADG512	QUAD, SPST, 30 ohm, +3V or ±5V RAIL, .5uWATT, NEGATIVE TRUE, Ton=200nsec
ADG513	QUAD, SPST, 30 ohm, +3V or ±5V RAIL, .5uWATT, 2 POSITIVE TRUE, 2 NEGATIVE TRUE, Ton=200nsec
ADG526A	16 Channel, 300 ohm, SINGLE ENDED, LATCHED,
ADG527A	8 Channel 300 ohm, DIFFERENTIAL, LATCHED,
ADG528A	8 Channel, 300 ohm, SINGLE ENDED, LATCHED, See ADG428 for new designs
ADG528F	8 Channel, 300 ohm, SINGLE ENDED, LATCHED, OVER VOLTAGE PROTECTED +/-20V OVER THE RAIL
ADG529A	4 Channel, 300 ohm, DIFFERENTIAL, LATCHED, See ADG429 for new designs
ADG608	8 Channel, 40 Ohm, SINGLE ENDED, +3V, +5V. or +/-5V, 3uW, 40nsec
ADG609	4 Channel, 40 Ohm, DIFFERENTIAL, +3V, +5V or +/-5V, 3uW, 40nsec
ADG701	SINGLE SPST, Ron=1 ohm, tON=20nsec, tOFF=10nsec, OFF ISOLATION=80dB, Sitch Normally Closed. CMOS, Vdd=+2.0 to 5.5V

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADG702	SINGLE SPST, Ron=1 ohm, tON=20nsec, tOFF=10nsec, OFF ISOLATION=80dB, Sitch Normally Open. CMOS, Vdd=+2.0 to 5.5V
ADG719	SINGLE SPDT, Ron=2 ohm, 100MHZ, tON=20nsec, tOFF=10nsec, CMOS, Vdd=+2.7 to 5V
ADG721	DUAL, SPST, 7 ohm, Vlogic=+1.85V to +5.5V,
ADG722	DUAL, SPST, 7 ohm, Vlogic=+1.85V to +5.5V,
ADG723	DUAL, SPST, 7 ohm, Vlogic=+1.85V to +5.5V,
ADG774	QUAD 2:1 VIDEO MUX, 2ohm flatness, CMOS, Vdd=3.3 to 5V, t ON/OFF/10nsec, BW=100MHz
ADM1181	RS-232, 2 DRIVERS, 2 RECEIVERS, 230 KBPS, 0.1 uF CAPS, LOW POWER, w/ HIGH EMI IMMUNITY, 15KV ESD, V+ .1uF cap to Gnd instead of to +Vcc
ADM1232	Vcc Monitor, Watchdog Timer 140>1200msec, Manual Reset, /RST & rst outputs, DS1232 & DS1232LP 2ND SOURCE
ADM1345	SINGLE SUPPLY +5V V.35 PORT, 3 DRIVERS, 3 RECEIVERS, 10KV ESD, PIN SELECT DCE or DTE MODE, Fail Safe Transmitter outputs high Impedance with power off.
ADM1385	RS-232, 2 DRIVERS, 2 RECEIVERS, 0,1uF CAPS, 230 KBPS, 2 Rcv Active in S/down, Tri-Stateable Rcv's, +3.3V Voltage Doubler, LTC1385 Pinout
ADM14185E	RS-232 & V.28, 3 DRIVERS, 5 RECEIVERS, Tri Supply +5V & +/-12V Rails, 460KBPS, EMI/ESD robust
ADM14196E	RS-232 & V.28, 5 DRIVERS, 3 RECEIVERS, Tri Supply +5V & +/-12V Rails, 460KBPS, EMI/ESD robust
ADM1485	RS-485, 30 MB/sec, SINGLE, LOW POWER Iq=1mA w/OUTPUTS DISABLED
ADM202	RS-232, 2 DRIVERS, 2 RECEIVERS, 120 KBPS, 0.1 uF CAPS, LOW POWER
ADM202E	RS-232, 2 DRIVERS, 2 RECEIVERS, 500 KBPS, 0.1 uF CAPS, LOW POWER, w/ HIGH EMI IMMUNITY, 15KV ESD
ADM203	RS-232, 2 DRIVERS, 2 RECEIVERS, INTERNAL CAPS, 120KBPS
ADM206	RS-232, 4 DRIVERS, 3 RECEIVERS, LOW POWER SHUTDOWN, 0.1uF CAPS, 120KBPS, w/THREE STATE
ADM207	RS-232, 5 DRIVERS, 3 RECEIVERS, 0.1uF CAPS, 120 KBPS
ADM207E	RS-232, 5 DRIVERS, 3 RECEIVERS, 0.1uF CAPS, 230 KBPS, HIGH EMI TOLERANCE & 15KV ESD
ADM208	RS-232, 4 DRIVERS, 4 RECEIVERS, 0.1uF CAPS, 120 KBPS
ADM208E	RS-232, 2 DRIVERS, 4 RECEIVERS, 0.1uF CAPS, 230 KBPS, HIGH EMI TOLERANCE & 15KV ESD

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADM209	RS-232, 3 DRIVERS, 5 RECEIVERS, 0.1uF CAPS, 200 KBPS
ADM211	RS-2321, 4 DRIVERS, 5 RECEIVERS, 0.1uF CHARGE PUMP CAPS, w/shutdown, w/tri-state enable
ADM211E	RS-232, 4 DRIVERS, 5 RECEIVERS, 0.1uF CHARGE PUMP CAPS, w/shutdown, w/tri-state enable, HIGH EMI IMMUNITY, 15KV ESD, 230KSBS
ADM213	RS-232, 4 DRIVERS, 5 RECEIVERS, 0,1uF CAPS, 120 KBPS, 2 RCV's ACTIVE IN s/DOWN, w/THREE STATE
ADM213E	RS-232, 4 DRIVERS, 5 RECEIVERS, 0,1uF CAPS, 230 KBPS, 2 RCV's ACTIVE IN s/DOWN, w/THREE STATE, HIGH EMI IMMUNITY, 15KV ESD
ADM222	RS-232, 2 DRIVERS, 2 RECEIVERS, 200 KBPS, 0.1 uF CAPS, LOW POWER, w/SHUTDOWN Iq=1uA
ADM223	RS-232, 2 DRIVERS, 2 RECEIVERS, 120 KBPS, 1 uF CAPS, LOW POWER, w/THREE STATE
ADM230L	RS-232, 5 DRIVERS, 0 RECEIVERS, 100KBPS, 1.0 uF CAPS, LOW POWER SHUTDOWN
ADM231L	RS-232, 2 DRIVERS, 2 RECEIVERS, 100KBPS, 1.0uF CAPS, LOW POWER
ADM232A	RS-232, 2 DRIVERS, 2 RECEIVERS, 200 KBPS, 0.1 uF CAPS, LOW POWER
ADM232L	RS-232, 2 DRIVERS, 2 RECEIVERS, 100 KBPS, 1 uF CAPS, LOW POWER
ADM233L	RS-232, 2 DRIVERS, 2 RECEIVERS, INTERNAL CAPS, 100KBPS, LOW POWER
ADM234L	RS-232, 4 DRIVERS, 0 RECEIVERS, 1uF CAPS, 100KBPS
ADM236L	RS-232, 4 DRIVERS, 3 RECEIVERS, LOW POWER SHUTDOWN, 1uF CAPS, 100KBPS, w/THREE STATE, LOW POWER
ADM237L	RS-232, 5 DRIVERS, 3 RECEIVERS, 1uF CAPS, 100 KBPS, LOW POWER
ADM238L	RS-232, 4 DRIVERS, 4 RECEIVERS, 1uF CAPS, 100 KBPS, LOW POWER
ADM239L	RS-232, 3 DRIVERS, 5 RECEIVERS, 1uF CAPS, 100 KBPS, LOW POWER
ADM241L	RS-232, 4 DRIVERS, 5 RECEIVERS, 1uF CAPS, 100 KBPS, LOW POWER
ADM242	RS-232, 2 DRIVERS, 2 RECEIVERS, 200 KBPS, 0.1 uF CAPS, LOW POWER, with SHUTDOWN Iq=100uA & ENABLE, RCV ACTIVE IN s/DOWN
ADM3202	RS-232, 2 DRIVERS, 2 RECEIVERS, 0,1uF CAPS, 230 KBPS, +3.3V Voltage Doubler
ADM3222	RS-232, 2 DRIVERS, 2 RECEIVERS, 0,1uF CAPS, 230 KBPS, 2 Rcv Active in S/down, Tri-Stateable Rcv's, +3.3V Voltage Doubler

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADM3491	FULL DUPLEX RS-485, +3.3V @Iq=840uA, 1uA in s/down, Drive 32 transceivers, Thermal Shutdown forces outputs to Three State
ADM483E	RS-485, 15KV ESD, HIGH EMI IMMUNITY, Low Slew Rate 250 KB/sec , SINGLE, Iq=2.2/1mA OUTPUTS ENABLED/DISABLED, HALF DUPLEX
ADM485	RS-485, 10 MB/sec, SINGLE, Iq=2.2/1mA OUTPUTS ENABLED/DISABLED, HALF DUPLEX
ADM488	RS-485, FULL DUPLEX TRANSCEIVER, +5V @ Iq=120uA, 10uA in S'Down, 12K Input Impedance, w/overtemp shutdown & receiver input open detection
ADM489	RS-485, FULL DUPLEX TRANSCEIVER, +5V @ Iq=120uA, 10uA in S'Down, 12K Input Impedance, w/overtemp shutdown & receiver input open detection, w/rec/drv enables
ADM5104	QUAD NRZ to TRANSFORMER, FAST ETHERNET 100 BASE TX TRANSCEIVER
ADM5170	OCTAL RS-232 or, RS423 LINE DRIVER. PINS WITH UNITRODE UC5170
ADM5180	OCTAL RS-232 or, RS423 LINE RECEIVER. PINS WITH UNITRODE UC5180
ADM560	EIA/TIA-562 STD. 4 DRV, 2 RCV's, +3V, 1.0uF CHARGE PUMP CAPS, 2 RCV. ACTIVE IN S/DOWN.
ADM561	EIA/TIA-562 STD. 4 DRV, 2 RCV's, +3V, 1.0uF CHARGE PUMP CAPS, RCV DISABLED in shutdown
ADM6315	uPROCESSOR RESET GENERATOR (ADM811) w/Manual Reset, Active Low, Iq=17uA, +4.63, +4.38, +3.08, +2.93, +2.63V VERSIONS, FACTORY TRIMMED TIMEOUTS, SOT-143 4 PIN
ADM660	CHARGE PUMP INVERTER or DOUBLER +2.5V > +7. , to +5. to +14V, or, +1.2>+.7 to -1.2>-7V, EXT OSC 25>120khz., Iout=100 mA, UNREGULATED OUTPUT
ADM663	SINGLE OUTPUT, DUAL MODE,+2 to +16V INPUT, OUTPUT FIXED +5V or adjustable, +1.3 to +16V, Iq=12uA, Iout=40mA
ADM663A	SINGLE OUTPUT, TRI MODE, +3.3V, +5V, ADJUSTABLE +2 to +16V, Iout=100mA
ADM666	SINGLE OUTPUT, DUAL MODE, +2 to +16V INPUT, OUTPUT FIXED +5V or adjustable, +1.3 to +16V, Iq=12uA, Iout=40mA, w/ BATTERY MONITOR OUTPUT
ADM666A	SINGLE OUTPUT, TRI MODE, +3.3V, +5V, ADJUSTABLE +2 to +16V, Iout=100mA, w/ BATTERY MONITOR OUTPUT
ADM690	Vcc & Vbatt Monitor +4.65V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.25>1.35V, Iout=100mA, Reset=50msec, Vcc=4.5>5.5V
ADM690A	Vcc & Vbatt Monitor +4.65V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.2>1.3V, Reset=200msec, Vcc=4.75>5.5V+/-5%, Iout=250mA, Improved adm690, allows Vbatt to float

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADM691	Vcc & Vbatt Monitor +4.65V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.25>1.35V, Iout=100mA, Reset=50msec, Vcc=4.75>5.5V
ADM691A	Vcc & Vbatt Monitor +4.65V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.2>1.3V, Iout=250mA, Reset=200msec, Vcc=4.75>5.5V
ADM692	Vcc & Vbatt Monitor +4.4V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.25>1.35V, Reset=50msec, Vcc=4.5>5.5V, Iout=100mA
ADM692A	Vcc & Vbatt Monitor +4.4V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.2>1.3V, Reset=200msec, Vcc=4.5>5.5V, Iout=250mA, Improved adm690, allows Vbatt to float
ADM693	Vcc & Vbatt Monitor +4.4V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.25>1.35V, Iout=100mA, Reset=50msec, Vcc=4.5>5.5V
ADM693A	Vcc & Vbatt Monitor +4.4V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.2>1.3V, Iout=250mA, Reset=200msec, Vcc=4.5>5.5V
ADM694	Vcc & Vbatt Monitor w/Watchdog Timer & on chip Battery Switch, Reset=100msec, Vcc=5V +/-5%
ADM695	Vcc & Vbatt Monitor w/Watchdog Timer, Ram Write Protect & on chip Battery Switch, Reset=280msec, Vcc=5V +/-5%
ADM696	Vcc & Vbatt Monitor w/Watchdog Timer, Ram Write Protect & on chip Battery Switch, Reset=70msec, Vcc=3V to +5.5V
ADM697	Vcc & Vbatt Monitor w/Watchdog Timer, Ram Write Protect, Reset=70msec, Vcc=3V to +5.5V
ADM698	+5V MONITOR, GENERATES RESET ON POWER UP/DOWN, OUTPUT LOW to Vdd=+1V,
ADM699	+5V MONITOR, GENERATES RESET ON POWER UP/DOWN, OUTPUT LOW to Vdd=+1V
ADM705	uPROCESSOR RESET GENERATOR, RESET Vh=4.65V, WATCHDOG TIMER IN/OUT
ADM706	uPROCESSOR RESET GENERATOR, RESET Vh=4.40V, WATCHDOG TIMER IN/OUT
ADM706P	uPROCESSOR RESET GENERATOR, RESET Vh=2.63V, WATCHDOG TIMER IN/OUT, RESET NEGATIVE TRUE
ADM706R	uPROCESSOR RESET GENERATOR, RESET Vh=2.63V, WATCHDOG TIMER IN/OUT, RESET POSITIVE TRUE
ADM706S	uPROCESSOR RESET GENERATOR, RESET Vh=2.93V, WATCHDOG TIMER IN/OUT, RESET NEGATIVE TRUE
ADM706T	uPROCESSOR RESET GENERATOR, RESET Vh=3.08V, WATCHDOG TIMER IN/OUT, RESET NEGATIVE TRUE
ADM707	uPROCESSOR RESET GENERATOR, RESET Vh=4.65V
ADM708	uPROCESSOR RESET GENERATOR, RESET Vh=4.40V

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADM708R	μPROCESSOR RESET GENERATOR, RESET Vh=2.63V
ADM708S	μPROCESSOR RESET GENERATOR, RESET Vh=2.93V
ADM708T	μPROCESSOR RESET GENERATOR, RESET Vh=3.08V
ADM709	μPROCESSOR RESET GENERATOR, RESET L/M/R/S/T=4.65/4.4/2.63/2.93/3.08Vh, M=4.4V, , RESET DELAY=20μsec, timeout=280msec typ
ADM800	Vcc & Vbatt Monitor L=+4.65V, M=+4.4V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.225>1.275V, Iout=250mA, Reset=200msec, Vcc=4.75>5.5V
ADM802L	Vcc & Vbatt Monitor +4.65V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.225>1.275V, Reset=200msec, Vcc=4.75>5.5V+/-5%, Iout=250mA, Improved adm690, allows Vbatt to float
ADM802M	Vcc & Vbatt Monitor +4.4V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.225>1.275V, Reset=200msec, Vcc=4.5>5.5V+/-5%, Iout=250mA, Improved adm690, allows Vbatt to float
ADM805L	Vcc & Vbatt Monitor +4.65V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.2>1.3V, Reset=200msec, Vcc=4.75>5.5V+/-5%, Iout=250mA, Improved adm690, allows Vbatt to float
ADM805M	Vcc & Vbatt Monitor +4.4V, w/Watchdog Timer & on chip Battery Switch, Powe fail Input +1.2>1.3V, Reset=200msec, Vcc=4.5>5.5V+/-5%, Iout=250mA, Improved adm690, allows Vbatt to float
ADM809	μPROCESSOR RESET GENERATOR, Active Low, Iq=17μA, +3,+3.3,+5V VERSIONS, 140msec, SOT-23 3 PIN
ADM810	μPROCESSOR RESET GENERATOR, Active High, Iq=17μA, +3,+3.3,+5V VERSIONS, 140msec, SOT-23 3 PIN
ADM811	μPROCESSOR RESET GENERATOR w/Manual Reset, Active Low, Iq=17μA, +4.63, +4.38, +3.08, +2.93, +2.63V VERSIONS, 140msec, SOT-143 4 PIN
ADM812	μPROCESSOR RESET GENERATOR w/Manual Reset, Active High, Iq=17μA, +4.63, +4.38, +3.08, +2.93, +2.63V VERSIONS, 140msec, SOT-143 4 PIN
ADM8660	CHARGE PUMP INVERTER +1.5V > +7. , to -1.2>-7V, INT OSC 25>120KHz., w/SHUTDOWN, Iout=100 mA, UNREGULATED OUTPUT
ADM8690	Vcc & Vbatt Monitor w/Watchdog Timer & on chip Battery Switch, Reset=50msec, Vcc=5V +/-5%, Improved adm69x series, better Vref & Osc accuracy
ADM8691	Vcc & Vbatt Monitor w/Watchdog Timer, Ram Write Protect & on chip Battery Switch, Reset=70msec, Vcc=5V +/-5%, Improved adm69x series, better Vref & Osc accuracy
ADM8692	Vcc & Vbatt Monitor w/Watchdog Timer & on chip Battery Switch, Reset=50msec, Vcc=5V +/-10%, Improved adm69x series, better Vref & Osc accuracy

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADM8693	Vcc & Vbatt Monitor w/Watchdog Timer, Ram Write Protect & on chip Battery Switch, Reset=70msec, Vcc=5V +/- 10%, Improved adm69x series, better Vref & Osc accuracy
ADM8694	Vcc & Vbatt Monitor w/Watchdog Timer & on chip Battery Switch, Reset=100msec, Vcc=5V +/-5%, Improved adm69x series, better Vref & Osc accuracy
ADM8695	Vcc & Vbatt Monitor w/Watchdog Timer, Ram Write Protect & on chip Battery Switch, Reset=280msec, Vcc=5V +/- 5%, Improved adm69x series, better Vref & Osc accuracy
ADM8696	Vcc & Vbatt Monitor w/Watchdog Timer, Ram Write Protect & on chip Battery Switch, Reset=70msec, Vcc=3V to +5.5V, Improved adm69x series, better Vref & Osc accuracy
ADM8697	Vcc & Vbatt Monitor w/Watchdog Timer, Ram Write Protect, Reset=70msec, Vcc=3V to +5.5V, Improved adm69x series, better Vref & Osc accuracy
ADM8698	+5V MONITOR, GENERATES RESET ON POWER UP/DOWN, OUTPUT LOW to Vdd=+1V, Improved adm69x series, better Vref & Osc accuracy
ADM8699	+5V MONITOR, GENERATES RESET ON POWER UP/DOWN, OUTPUT LOW to Vdd=+1V, Improved adm69x series, better Vref & Osc accuracy, w/watchdog timer input
ADM9240	6 Inputs, On Chip Temp Sensor, 5 digital inputs for VID Bits, 2 Fan Speed Monitoring Inputs, LDCM Support, I2C I/O, Prgm Reset/Overtemp S/down pin
ADM9261	TRIPLE PC VOLTAGE MONITOR, With manual Input, Iq=10uA, Vcc=+2.5>3.6V, Int hystersis, power Supply Glitch immunity, 8 Pin uSOIC
ADM9264	QUAD PC VOLTAGE MONITOR, +2.8V, +3.3V, +5V and +12V
ADM9268	QUAD PC VOLTAGE MONITOR, +2.8V, +3.3V, +5V and +12V, w/I2C INTERFACE
ADM9690	POWER SUPPLY & PRGM WATCH DOG TIMER 0.75/1.5/12.5/25 msec, DUAL ACTIVE LOW RESET OUTPUTS OFFSET BY 10msec
ADMC200	11 BIT, 4 CH SIMULTANEOUS, 3.2usec/CH, 12 bit three phase center based PWM Timer w/PRGM Deadtime & pulse Deletion. 2.5>25KHZ PWM Range, 12 Bit Vector Transformation
ADMC200EB	Evaluation board for ADCM200
ADMC201	11 BIT, 7 inputs, 4 CH SIMULTANEOUS, 3.2usec/CH, 12 bit three phase center based PWM Timer w/PRGM Deadtime & pulse Deletion. 2.5>25KHZ PWM Range, 12 Bit Vector Transformation, 6 bit configurable Digital I/O w/Change of state interrupt support
ADMC300	Single Chip DSP Motor Controller incorporating the ADSP-2171 DSP core, Seven Channel ADC, 12-Bit PWM Generator, Encoder Interface, 12 Bit I/O

[Click on a part to see Datasheet.](#)



# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADMC330	Single Chip DSP Motor Controller incorporating the ADSP-2171 DSP core, Seven Channel ADC, 12-Bit PWM Generator, 8 Bit I/O
ADMOD79	SIGMA/DELTA MODULATOR, 18 BIT, DUAL, 64X OVERSAMPLING @ Fclk=3.072MHz, BW=20KHZ
ADP1073	STEP UP or STEP DOWN, Vin=+1.5V, ADJUSTABLE, or +5 /+12V, Ignd1305uA, INT 1Amp Switch, w/Low Battery Detector, 19KHz, DUTY CYCLE 72%, 40mA @5V from =1.24V, Ton=38uA, Unreleased New Produ
ADP1108	STEP UP or STEP DOWN, +2V to +30V, ADJUSTABLE, 3.3 +5 and +12V, Iq=110uA, w/Auxiliar Gain block, 19KHz, DUTY CYCLE 70%, Ton=36uA, 150mA @5V From +3V, Interna 1 AMP Switch
ADP1109	STEP UP, +2V to +30V, ADJUSTABLE, +5 and +12V, Iq=550uA, Ignd=320uA, w/Shutdown, 120KHz, DUTY CYCLE=50%, Ton=4.2uA, 100mA @ +5V From +3V, Internal 1Amp Switch, Vin=2 to 12V
ADP1109A	STEP UP, +2V to +30V, ADJUSTABLE, +5 and +12V, Iq=550uA, Ignd=320uA, w/Shutdown, 120KHz, DUTY CYCLE=50%, Ton=4.2uA, 100mA @ +5V From +3V, Internal 1Amp Switch, Vin=2 to 9V
ADP1110	STEP UP or STEP DOWN, Vin=+1>30V, ADJUSTABLE and +3.3V +5V,+12V Fixed, Iq=300uA, w/Low Battery Detector, 70KHz, DUTY CYCLE 69%, Ton=10uA, Internal 1 A Switch
ADP1111	STEP UP or STEP DOWN, +2V to +30V, ADJUSTABLE and 3.3V +5V and +12V Fxed, Iq=500uA, w/Low Battery Detector, 70KHz, DUTY CYCLE 50%, Ton=7uA, 100mA @5V from +3V, Internal 1 Amp Switch
ADP1147	+3.3V or +5V STEP-DOWN CURRENT MODE OPERATION, +3.5V +16V INPUT, Istby=160uA, Iq in S'Down=22uA, 250KHZ Constant Off Time, Eff=95%, P Drive=50mA
ADP1148	+3.3V or +5V STEP-DOWN SYNCHRONOUS MODE OPERATION, +3.5V +20V INPUT, 250KHZ, P (primary) & N (seconadry) non overlap Drives, Iout=1mA>10A,
ADP1173	STEP UP or STEP DOWN, +2V to +30V, ADJUSTABLE, 3.3 +5 and +12V, Iq=110uA, w/Low Battery Detector, 24KHz, DUTY CYCLE 55%, Ton=23uA, 80mA @5V from +3V
ADP3000	STEP UP or STEP DOWN, +2V to +30V, ADJUSTABLE, 3.3 +5 and +12V, Iq=110uA, w/Low Battery Detector, 400KHz, DUTY CYCLE 90%, Ton=2uA, 120mA @3.3V from +5V
ADP3050	+3.3V or +5V or ADJ. STEP-DOWN, anyCAP, Acc=+/-2.5% , CURRENT MODE OPERATION, Vin=+3.6V to +30V, Iout=1.2A, Iq=4mA, Iq in S'Down=20uA, 300KHZ Constant Off Time,
ADP3152	SWITCHING REGULATOR CONTROLLER, CONSTANT OFF TIME, DUAL N Channel SYNCHRONOUS DRIVER 250KHZ, 5 BIT PROGRAMMABLE, +1.3 to +3.5V OUTPUT, FOR PENTIUM PRO
ADP3153	SWITCHING REGULATOR CONTROLLER, CONSTANT OFF TIME, DUAL N Channel SYNCHRONOUS DRIVER 250KHZ, 5 BIT PROGRAMMABLE, +1.3 to +3.5V OUTPUT, FOR PENTIUM PRO or II

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADP3300	SINGLE OUTPUT, AnyCap, +2.7, 3, 3.2, 3.3, 5V, 1%, Iout=50mA, Step Down, Vin=2>16V, 400KHz, GND Current=1mA, Iq=1uA in shutdown, w/Low Batt Detect. Adj. Current Limit, Dropout=100mV
ADP3301	SINGLE OUTPUT, AnyCap, +2.7, 3, 3.3, 5V, 1%, Iout=100mA, GND Current=2mA, Iq=1uA in shutdown, Vin=3 to 20V, Dropout=120mV
ADP3302	DUAL OUTPUT, AnyCap, Combinations of +2.7, 3, 3.3, 5V, 1%, Iout=100mA, GND Current=2mA, 1 GND Pin, Iq=1uA in shutdown, Vin=3 to 20V, Dropout=100mV
ADP3303	SINGLE OUTPUT, AnyCap, +2.7, 3, 3.2, 3.3, 5V, 0.5%, Iout=250mA, Vin=3.5 to 20V
ADP3306	SINGLE OUTPUT, AnyCap, 1% ACCURACY, Iout=300mA LDO Regulator; Vin= 3.2>12V, Vout: 2.7V, 3.0V, 3.2V, 3.3V, 5V.
ADP3307	SINGLE OUTPUT, AnyCap, 0.8% ACCURACY, Iout=100mA LDO Regulator 160mV @ 100mA; Vin= 3>12V, Vout: 2.7V, 3.0V, 3.3V
ADP3308	SINGLE OUTPUT, AnyCap, +2.7, 3, 3.2, 3.3, 5V, 0.8%, Iout=50mA, Step Down, Vin=2>16V, 400KHz, GND Current=1mA, Iq=1uA in shutdown, w/Low Batt Detect. Adj. Current Limit, Dropout=100mV, Pins with LP2980
ADP3309	SINGLE OUTPUT, AnyCap, 0.8% ACCURACY, Iout=100mA LDO Regulator 160mV @ 100mA; Vin= 3>12V, Vout: 2.7V, 3.0V, 3.3V, Pins with LP2950
ADP3310	POSITIVE CONTROLLER, +5V Input, Vout=+2.8, 3.0, 3.3, 5.0 @ 5.6Amps w/EXT PassTransistor IRFR9024, w/Sdown
ADP3367	SINGLE OUTPUT, DUAL MODE, +3.5 to +16.5V INPUT, OUTPUT FIXED +5V 2% or adjustable, +1.3 to +16V, Iq=17uA, Iout=200mA, Vdropout=150/300mV @ 200/300mA, w/S'Down Iq=.2uA, ESD>6000V
ADP3603	CHARGE PUMP, SWITCH CAP (10uF 120KHz) VOLTAGE CONVERTER, Vin=+4.5V to +6V, -3.0V OUT @ 2%, 50mA, REGULATED OUTPUT, Iq=10mA, 1mA in shutdown
ADP3604	CHARGE PUMP, SWITCH CAP (10uF 120KHz) VOLTAGE CONVERTER, Vin=+4.5V to +6V, -3.0V OUT @ 2%, 120mA, REGULATED OUTPUT, Iq=10mA, 1mA in shutdown
ADP3800	BATTERY CHARGER FOR Lilon, MicCad, and NiMH; 100kHz; Pin Programmable Chemistry and Cell Number Select, w/on chip LDO, Drives ext. PMOS FET
ADP3801	BATTERY CHARGER FOR Lilon, MicCad, and NiMH; 200kHz; Pin Programmable Chemistry and Cell Number Select, w/on chip LDO, Drives ext. PMOS FET
ADP3802	BATTERY CHARGER FOR Lilon, MicCad, and NiMH; 500kHz; Pin Programmable Chemistry and Cell Number Select, w/on chip LDO, Drives ext. PMOS FET
ADP3810	BATTERY CHARGER FOR LITHIUM ION, INTERNAL RESISTOR DIVIDER TRIMMED , Vout OPTIONS=+4.2, +8.4, +6.0, +16.8, 1% Accuracy

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADP3811	BATTERY CHARGER NiCad or NiMH, EXTERNAL RESISTORS SET THE VOLTAGE LIMIT, 2% Accuracy
ADP667	SINGLE OUTPUT, DUAL MODE, +3.5 to +16.5V INPUT, OUTPUT FIXED +5V 4% or adjustable, +1.3 to +16V, Iq=20uA, Iout=200mA, V drop out=350mV
ADR290	+2.048V, Iq=10uA, Initial Accuracy-1/2/5mV, Iout=5mA, XFET low noise/Hysteresis
ADR291	+2.50V, Iq=10uA, Initial Accuracy-1/2/5mV, Iout=5mA, XFET low noise/Hysteresis
ADR292	+4.0960V, Iq=10uA, Initial Accuracy-1/2/5mV, Iout=5mA, XFET low noise/Hysteresis
ADR293	+5.0V, Iq=15uA, Initial Accuracy-0.1%/0.5%, Iout=5mA, XFET low noise/Hysteresis
ADSP2100	16 BIT, FIXED POINT, 120 nsec, FIRST GENERATION, See ADSP2101 for new designs
ADSP2100A	16 BIT, FIXED POINT, 100 nsec, 2ND GENERATION, See ADSP2101 for new designs
ADSP2101	16 BIT, FIXED POINT, 2K PM, 1K DM, 16.7,20,25 MHz, 2 Serial PORTS
ADSP21020	32 BIT, 40 BIT EXTENDED, IEEE FLOATING, 20, 25, 30, 33 MHz, 223 PINS PGA, See ADSP21061 for new designs
ADSP2103	AD2101 SELECTED FOR +3.3V OPERATION, 10,12.5,25 MHz
ADSP2104	16 BIT, FIXED POINT, 1/2K PM, 1/4 K DM, 20MIPS, 2 Serial PORTS, 1 TIMER, L Version=+3.3V 13.824 MIPS
ADSP2105	16 BIT, FIXED POINT, 1K PM, 0.5K DM, 10, 12.5MHz, 1 Serial PORTS
ADSP21060	32 BIT, 40 BIT EXTENDED, IEEE FLOATING, 4MBIT RAM, 33.3>60MHz
ADSP21060L	32 BIT, 40 BIT EXTENDED, IEEE FLOATING, 4MBIT RAM, 33.3>60MHz, +3V Operation
ADSP21061	32 BIT, 40 BIT EXTENDED, IEEE FLOATING, 1MBIT RAM, 33.3MHZ
ADSP21061L	32 BIT, 40 BIT EXTENDED, IEEE FLOATING, 1MBIT RAM, 33.3/40MHZ, =3.3V
ADSP21062	32 BIT, 40 BIT EXTENDED, IEEE FLOATING, 2MBIT RAM, 40MHz,
ADSP21062L	32 BIT, 40 BIT EXTENDED, IEEE FLOATING, 2MBIT RAM, 40MHz, 3V Operation
ADSP2111	16 BIT, FIXED POINT, AD2101 WITH uCOMPUTER HOST INTERFACE PORT, 12.5, 16.7, 20MHz
ADSP2115	16 BIT, FIXED POINT, 1K PM, 1/2 K DM, 16.7, 20, 25 MHz, 2 Serial PORTS
ADSP2161	ADSP2101 CORE, WITH 8K PM ROM, 1/2K DM ROM, 2 Serial PORTS, 10, 12, 16.7 MHz
ADSP2162	ADSP2161, +3.3V OPERATION, 10 MHz

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADSP2163	ADSP2101 CORE, WITH 4K PM ROM, 1/2K DM RAM, 2 Serial PORTS, 10, 12, 16.6 MHz
ADSP2164	ADSP2163, +3.3V OPERATION, 10 MHz
ADSP2165	ADSP2101 CORE, WITH 12K PM ROM, 4K DM RAM, 1K PM RAM, 2 Serial PORTS, 20 MHz
ADSP2166	ADSP2101 CORE, WITH 12K PM ROM, 4K DM RAM, 1K PM RAM2 Serial PORTS, 20 MHz, +3.3V
ADSP2171	AD2101 CORE+2K DM RAM, 2K PM RAM, 8K PM ROM, 26, 33 MIPS
ADSP2173	ADSP2171, +3.3V OPERATION, 20 & 33 MIPS
ADSP2181	AD2111 CORE+16K DM RAM, 16K PM RAM, DMA PORT, 33/40MIPS, LONG D/S ON FAXBACK
ADSP2183	AD2111 CORE+16K DM RAM, 16K PM RAM, DMA PORT, 28MIPS, +3V
ADSP2185	AD2111 CORE+16K DM RAM, 16K PM RAM, DMA PORT, 28/ 33MIPS, ADSP2181 in 100 pin TQFP, IDMA or BDMA selectable at power up
ADSP2185L	AD2111 CORE+16K DM RAM, 16K PM RAM, DMA PORT, 28/ 33MIPS, ADSP2181 in 100 pin TQFP, IDMA or BDMA selectable at power up, +3V
ADSP2186	AD2111 CORE+8K DM RAM, 8K PM RAM, DMA PORT, 33MIPS, ADSP2181 in 100 pin TQFP, IDMA or BDMA selectable at power up
ADSP2186L	AD2111 CORE+8K DM RAM, 8K PM RAM, DMA PORT, 33MIPS, ADSP2181 in 100 pin TQFP, IDMA or BDMA selectable at power up, +3V
ADSP2187L	AD2111 CORE+32K DM RAM, 32K PM RAM, DMA PORT, 50MIPS, +3.3V
ADSP21MOD8	SINGLE CHIP DIGITAL MODEM FOR REMOTE ACCESS SEVERs, Dash -100 Version comes with bundled software
ADSPMSP58	Mixed Signal, ADSP2171 CORE, 2K PM, 2K DM, + 16 BIT CODEC, 26MSPS
ADSPMSP59	Mixed Signal, ADSP2171 CORE, 2K PM, 2K DM, 4K ROM, + 16 BIT CODEC, 26MSPS
ADSST-DAM10	ADAM-200, REFERENCE DESIGN FOR A ANSWERING MACHINE (GLOSSY), H3005-5-2/97
ADSST-DEC10	VIDEO AUTHORIZING CHIP SET, MPEG-I AUDIO, G.728, ACOUSTIC CANCELLATION (GLOSSY)
ADSST-EM100	REFERENCE DESIGN FOR A SINGLE PHASE POWER METER AND HARMONIC ANALYZER (GLOSSY)
ADSST-EM300	REFERENCE DESIGN FOR A THREE PHASE POWER METER AND HARMONIC ANALYZER (GLOSSY)
ADSST-G723	SPEECH COMPRESSION G.723.1 (GLOSSY), H3008-4-4/97

[Click on a part to see Datasheet.](#)

# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADSST-G728	SPEECH COMPRESSION G.728 (GLOSSY)
ADSST-G729	SPEECH COMPRESSION G.729 (GLOSSY)
ADSST-MOD2	V.34 DATA PUMP REFERENCE DESIGN, ADSP2181+AD1843+MODEM LICENSE (GLOSSY), H3009-6.5-3/97
ADSST-MOD2	V.34 DATA PUMP REFERENCE DESIGN, ADSP2181+MODEM LICENSE (GLOSSY), H3009-6.5-3/97
ADSST-MPEG	MPEG I CODER AND DECODER CHIP SET
ADSST-MPEG	MPEG-II CHIP SET
ADSST-NAV20	CHIP SET FOR GPS RECEIVER, SHARC BASED (GLOSSY), H3006-5-2/97
ADSST-NAV21	CHIP SET FOR GPS RECEIVER, 2 ADSP2181+SRAM (GLOSSY), H3007-5-2/97
ADSST-VC200	REFERENCE DESIGN FOR VIDEO PHONE, ADSP21061 BASED FOR VIDEO, AUDIO ADSP2181+AD1843
ADSST-VC300	REFERENCE DESIGN FOR VIDEO PHONE, HOST BASED FOR VIDEO, AUDIO ADSP2181+AD1843, H3068-6.5-5/97
ADSST-WLL10	corDECT REFERENCE DESIGN, FOR DECT, DIGITAL INTERFACE UNIT SOFTWARE+ADSP2181 (GLOSSY), H3015-7.5-2/97
ADSST-WLL20	corDECT REFERENCE DESIGN, FOR DECT, BASE STATIONS, SOFTWARE+ADSP2181 (GLOSSY), H3015-7.5-2/97
ADSST-WLL30	corDECT REFERENCE DESIGN, FOR DECT, HANDSETS or WALLSETS, SOFTWARE+ADSP2181 (GLOSSY), H3015-7.5-2/97
ADT05	THERMOSTAT, RESISTOR PROGRAMMABLE, 1 OPEN COLLECTOR OUTPUT, RANGE -40 TO +150C, Vs=+2.7 to +7V, Iq=90uA, Accuracy=±2C, 4C hysteresis, AD22105 in a 5 LEAD SOT-23
ADT14	QUAD, SETPOINT TEMP. CONTROLLER, PIN STRAP HYSTERESIS, +4.5 to +13.2V, ±3C ACCURACY, Vref=+2.5V, VPTAT=5mV/C, Iq=500uA 10uA in shutdown
ADT45	Temperature Sensor, Vout=250mV, TC=10m/C, Iq=50uA, Accuracy=±2C, -40 to +125C, Vin=+2.7 to +12V, SOT-23, Pin Compatible with LM45
ADT50	Temperature Sensor, Vout=750mV, TC=10m/C, Iq=50uA, Accuracy=±2C, -40 to +125C, Vin=+2.7 to +12V, SOT-23, Pin Compatible with LM50
ADT70	PLATINUM (P)RTD SIGNAL CONDITIONER, w/DUAL 1 mA CURRENT SOURCES, Gain=1.3mV/ohm, RAIL to RAIL I/A, SPARE OP AMP, +5 or ±5V, Iq=3mA, w/shutdown Iq=10uA, Vref=+2.5V
ADV101	TRIPLE, 8 BIT, 80 MHz, Pin Compatible with BT101, See ADV7121 for new designs

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADV453	TRIPLE, 8 BIT, PSEUDO COLOR, 66 MHz, BT453 2ND SOURCE
ADV458	SINGLE, 8 BIT, PSEUDO COLOR, 80. 110. 125, 170 MHz: BT 458 2ND SOURCE
ADV471	6 BIT, TRIPLE PSEUDO COLOR, 100 MHz: BT471 2ND SOURCE
ADV473	SINGLE, 8 BIT, TRUE COLOR, 100 MHz: BT 473 2ND SOURCE
ADV476	TRIPLE, 6 BIT, PSEUDO COLOR, 66 MHz: BT 476 2ND SOURCE
ADV478	8 BIT, ADV471 TRIPLE PSEUDO COLOR: BT 478 2ND SOURCE
ADV601	350:1 REAL TIME COMPRESS/DECCOMRESS OF CCCIR-601 DIGITAL VIDEO
ADV601LC	350:1 REAL TIME COMPRESS/DECCOMRESS OF CCCIR-601 DIGITAL VIDEO
ADV7120	TRIPLE, 8 BIT, 30/50/80 MHz, RS-170
ADV7122	TRIPLE, 10 BIT, 30/50/80 MHz, RS-170, with SYNC & BLANK
ADV7123	TRIPLE, 8/10 BIT, 140MSPS, +3V or +5V, w/Vref & Power Down, AC Specs, Pins With, ADV7120/21/22
ADV7127	SINGLE, 8/ 10 BIT, 140MSPS, +3V or +5V, w/Vref & Power Down, AC Specs, Pins With, ADV7128
ADV7128	SINGLE, 8 BIT, 30/50/80 MHz, +5V @125mA, Iout=15mA
ADV7129	TRIPLE, 10 BIT, 360 MHz,192 BIT PIXEL PORT 8:1 MUX, TRUE COLOR WITH 64x64 CURSOR, 2000x2000x24,
ADV7150	TRIPLE, 10 BIT, TRUE COLOR , MUX PIXEL PORTS 1:1, 2:1, 4:1: 85,110, 135, 170 MHz
ADV7151	TRIPLE, 10 BIT, PSEUDO-COLOR , MUX PIXEL PORTS 1:1, 2:1, 4:1, 85, 110, 135, 170 MHz
ADV7152	TRIPLE, 10 BIT, TRUE COLOR , MUX PIXEL PORTS 1:1, 2:1,: 85, 110, 135, 170, 220 MHz
ADV7160	TRIPLE, 10 BIT, TRUE COLOR , and WINDOWS 64x64 HARDWARE CURSOR, 96 BIT PIXEL PORT (1600x1280x24), MHz, PLL, QFP
ADV7162	TRIPLE, 10 BIT, TRUE COLOR , and WINDOWS 64x64 HARDWARE CURSOR, 96 BIT PIXEL PORT (1600x1280x24), MHz, PLL, THERMALLY ENHANCED, QFP
ADV7170	NTSC/PAL VIDEO ENCODER, YUV OUTPUT
ADV7171	NTSC/PAL VIDEO ENCODER, YUV OUTPUT
ADV7174	DIGITAL RGB TO ANALOG NTSC or PAL B/D/G/H/I/M/N, 8/16 BIT VIDEO & I2C, CCIR656/601 COMPATIBLE, 4 10 BIT D/A's, 27MHz, MPEG -I & II COMPLIANT Input Port, w/8 COLOR ON SCREEN DISPLAY & CLOSED CAPTION

[Click on a part to see Datasheet.](#)

# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADV7175	DIGITAL RGB TO ANALOG NTSC or PAL B/D/G/H/I/M/N, 8/16 BIT VIDEO & I2C, CCIR656/601 COMPATIBLE, 4 10 BIT D/A's, 27MHz, MPEG -I & II COMPLIANT Input Port, w/MACROVISION 6.1 Pay Per View, MUST BE A LICENSEE, see AD7175A for new designs
ADV7175A	DIGITAL RGB TO ANALOG NTSC or PAL B/D/G/H/I/M/N, 8/16 BIT VIDEO & I2C, CCIR656/601 COMPATIBLE, 4 10 BIT D/A's, 27MHz, MPEG -I & II COMPLIANT Input Port, w/MACROVISION 6.1 Pay Per View, MUST BE A LICENSEE
ADV7176	DIGITAL RGB TO ANALOG NTSC or PAL B/D/G/H/I/M/N, 8/16 BIT VIDEO & I2C, CCIR656/601 COMPATIBLE, 4 10 BIT D/A's, 27MHz, MPEG -I & II COMPLIANT Input Port, see AD7176A for new designs
ADV7176A	DIGITAL RGB TO ANALOG NTSC or PAL B/D/G/H/I/M/N, 8/16 BIT VIDEO & I2C, CCIR656/601 COMPATIBLE, 4 10 BIT D/A's, 27MHz, MPEG -I & II COMPLIANT Input Port
ADV7177	DIGITAL RGB TO ANALOG NTSC or PAL B/D/G/H/I/M/N, 8/16 BIT VIDEO & I2C, CCIR656/601 COMPATIBLE, 3 9 BIT D/A's, 27MHz, MPEG -I & II COMPLIANT Input Port, w/8 COLOR ON SCREEN DISPLAY & CLOSED CAPTION
ADV7178	DIGITAL RGB TO ANALOG NTSC or PAL B/D/G/H/I/M/N, 8/16 BIT VIDEO & I2C, CCIR656/601 COMPATIBLE, 3 9 BIT D/A's, 27MHz, MPEG -I & II COMPLIANT Input Port, & Macrovision 7.01
ADV7185	COMPOSITE or S-VIDEO VIDEO DECODER/CAPTURE, 10 BIT A/D 57 dB SNR, CONVERT PAL/NTSC to YCrCb (4:2 or 4:1:1), w/3 Line Comb Filter, 6 INPUT PORTS, I2C CONTROL
ADVFC32	500 KHz FS, 0.2% LINEARITY @ 500KHZ, +/-15V RAILS @8mA
ADXL05	ACCELEROMETER, $\pm 1$ to $\pm 5$ g, DC to 45KHZ, 200mv or 1V/g FS, 5mg Resolution, 0.2% Linearity
ADXL05EM1	PREPACKAGED EVALUATION MODULES SINGLE AXIS +/- 4g, BUY RESALE OF NGT TECH 3 CROSS RD LAGRANGEVILL NY 12540, 914-223-3359
ADXL05EM3	PREPACKAGED EVALUATION MODULES TRI- AXIS +/- 4g, BUY RESALE OF NGT TECH 3 CROSS RD LAGRANGEVILL NY 12540, 914-223-3359
ADXL150	ACCELEROMETER, $\pm 50$ g, IMPROVED (OPEN LOOP) ADXL50, SNR=74dB @ 100Hz, Resolution=1mg @ 100hz, ZERO Drift=0.4g -40>+85C ie. Output ratiometric to + Rail.
ADXL150EM1	EVALUATION MODULE FOR THE ADXL150, SINGLE AXIS, +/-10mg to +/-10G, 100HZ preset bandwidth, +/-4 and 25g versions available.
ADXL150EM3	EVALUATION MODULE FOR THE ADXL150, TRI- AXIAL, +/-10mg to +/-10G, 100HZ preset bandwidth, +/-4 and 25g versions available.
ADXL250	ACCELEROMETER, 2 AXIS, $\pm 50$ g, IMPROVED (OPEN LOOP) ADXL50, SNR=74dB @ 100Hz, Resolution=10mg @ 100hz, ZERO Drift=0.4g -40>+85C ie. 10X Better than ADXL50
ADXL50	ACCELEROMETER, $\pm 1$ to $\pm 50$ g, DC to 45KHZ, 200mv or 1V/g FS, 5mg Resolution, 0.2% Linearity

[Click on a part to see Datasheet.](#)



## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
ADXL50EM1	PREPACKAGED EVALUATION MODULES SINGLE AXIS +/- 25g, B/R OF NGT TECH 3 CROSS RD LAGRANGEVILL NY 12540, 914-223-3359
ADXL50SP	5g Sample pack -- 2 IC's & 2 eval BD's
AMP01	RESISTOR PROGRAMMABLE, $V_{os}$ RTI=50uV, $I_{out}$ =50mA, $I_b$ =4nA, $A_v$ =1 to 10,000
AMP02	RESISTOR PROGRAMMABLE, $A_v$ =1 to 1000, 200khz BW @ $A_v$ =1000, $V_{os}$ RTI=100uV, $I_b$ =10nA, GOOD RF IMMUNITY
AMP03	25 K RESISTOR SUBTRACTOR, not a true I/A, $V_{os}$ =400uv, CMRR=85dB, gAIN ERROR=.008%, BW=3MHz, SLEW RATE=9.5V/uVsec
AMP04	RESISTOR PROGRAMMABLE, SINGLE or DUAL SUPPLY, HIGH CMV RANGE, $A_v$ =1 TO 1000, ZERO IN ZERO OUT
BUF04	100 MHz, BUFFER, 2000V/uVsec, 200mA PEAK 50 mA CONTINUOUS OUTPUT DRIVE, $\pm 15V$
CMP01	SINGLE, 92V/uVsec, low drift, +5V, 180nsec
CMP02	DUAL, 270 nsec with 100mV step, $V_{os}$ =-.8mV, $I_b$ =3nA, 15V/uVsec, CMRR=94dB, $V_s$ =+/-15V
CMP04	QUAD, PRECISION, Upgrade to LM139/239/339, $V_s$ =+5V or +/-15V, $V_{os}$ =1mV, $A_{vo}$ =80K, $I_b$ =10nA, 1500nsec W/100mV overdrive
CMP05	SINGLE, low drift, +5V or +/-5V, 55nsec
CMP401	QUAD, "DIGITAL ONLY" COMPARATOR, +3 or 5V= $V_{logic}$ , +3V to $\pm 5V$ = $V_{analog}$ , 23 nsec PROP DELAY w/5mV OVERDRIVE, $I_q$ =7.5mA
CMP402	QUAD, "DIGITAL ONLY" COMPARATOR, +3 or 5V= $V_{logic}$ , +3V to $\pm 5V$ = $V_{analog}$ , 65 nsec PROP DELAY w/5mV OVERDRIVE, $I_q$ =2.4mA
DAC08	SINGLE, 8 BIT, IND. STD., CURRENT OUTPUT, 85 nsec SETTling, $V_s$ =+/-15V @ +3.8/-7.8mA
DAC100	SINGLE, 10 BIT, CURRENT OUTPUT, 100 nsec Settling Time, See ADV7123 for new designs
DAC16	SINGLE, 16 BIT, CURRENT OUT, 500 nsec
DAC312	SINGLE, 12 BIT, $I_{out}$ =0 to 4mA, +5V and -15V Rails, 150nsec SETTling TIME to .01%, See AD566A for new designs
DAC8043	SINGLE, 12 BIT, INVERTED R2R, CURRENT OUTPUT, 3 WIRE Serial I/O, +5V, DAC8043
DAC8143	SINGLE, 12 BIT, INVERTED R2R, CURRENT OUTPUT, Serial IN, Serial OUT, +5V, See AD7390
DAC8221	DUAL, 12 BIT, CURRENT OUTPUT, 12 BIT I/O, SKINNY DIP VERSION OF DAC8212

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
DAC8222	DUAL, 12 BIT, CURRENT OUTPUT, 12 BIT I/O, DOUBLE BUFFERED
DAC8228	DUAL, 8 BIT, VOLTAGE OUTPUT, PINS WITH AD7528, +12 to +15V SINGLE SUPPLY
DAC8229	DUAL, 8 BIT, VOLTAGE OUTPUT, +15V, -5V RAILS
DAC8248	DUAL, 12 BIT, VOLTAGE OUTPUT, 8 BIT I/O, +5 to +15V, DOUBLE BUFFERED
DAC8408	QUAD, 8 BIT, INVERTED R/2R CURRENT OUTPUT
DAC8412	QUAD, 12 BIT, VOLTAGE OUT=Vref hi to Vref low, 12 BIT I/O, RESET to ZERO, w/readback
DAC8413	QUAD, 12 BIT, VOLTAGE OUT=Vref hi to Vref low, 12 BIT I/O, RESET to MIDSCALE, w/readback
DAC8420	QUAD, 12 BIT, VOLTAGE OUT=Vref hi to Vref low, w/CLEAR, 3 WIRE Serial I/O
DAC8426	QUAD, 8 BIT, VOLTAGE OUTPUT, INTERNAL +10V REFERENCE, PINS WITH AD7226
DAC8512	SINGLE, 12 BIT, +5V RAIL, Iq=1mA, COMPLETE, Serial I/O, 8 PINS
DAC8562	SINGLE, 12 BIT, +5V RAIL, Iq=1mA, COMPLETE, Parallel I/O, 8 PINS
DAC8800	OCTAL, 8 BIT, VOLTAGE SWITCHING R 2R, UNBUFFERED Vout, 3 WIRE Serial I/O
DAC8840	OCTAL, 8 BIT, Vout, 1 MHz 4 QM. BW, TRIMDAC, 3 WIRE Serial I/O
DAC8841	OCTAL, 8 BIT, Vout, 2 QM SS, 4QM DUAL SUPPLY, TRIMDAC, 3 WIRE Serial I/O
EVALAD7892-	EVALUATION BOARD, COMPATIBLE WITH EVAL-CONTROL BOARD, AD7892-2, \$250, BOARD INCLUDED
EVALCONTRO	GENERIC DSP BASED A/D (MOTHER ) CONTROLLER FOR GENERAL PURPOSE CONVERTER EVALUATION/DEMO BOARDS
HTS0010	SINGLE, Tacq=10/14 nsec to 1%/0.1%, Drop Rate=100mV/usec, +/-15V @36/48mA, See AD9100 or AD9101 for new designs
IS54/IS136	IF BASEBAND CONVERSION, AD7011+AD7013+AD607, H3018-7.5-2/97
MAT01	DUAL, NPN, Vos=100uV, TCVos=.5uV/c, Hfe=500, enoise=7.5nV/SQRTHz
MAT02	DUAL, NPN, Vos=50uV, TCVos=.1uV/c, Hfe=500, enoise=1nV/SQRTHz
MAT03	DUAL, PNP, Vos=100uV, TCVos=.5uV/c, Hfe=100, enoise=1nV/SQRTHz
MAT04	QUAD, NPN, Vos=200uV, TCVos=1uV/c, Hfe=300, enoise=2.5nV/SQRTHz

[Click on a part to see Datasheet.](#)

# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
MC/BVAD7002	Evaluation board for the AD7002
MLT04	QUAD, +/-5V, 8MHz, LINEARITY=.2%, 4 QUADRANT, $V_{out} = X*Y/2.5V$ ,
MUX08	8 Channel, 300 or 400 ohm, SINGLE ENDED, WITH OVER VOLTAGE PROTECTION, JFET
MUX16	16 Channel, 380 or 580 ohm, OVER VOLTAGE PROTECTED, JFET
MUX24	4 Channel, 300 or 400 ohm, DIFFERENTIAL, JFET
MUX28	8 Channel, 380 or 580 ohm, DIFFERENTIAL, NOT LATCHED, JFET
OP02	SINGLE, PREMIUM 741 REPLACEMENT, $V_{os}=.5>5mV$ , $I_b=30>100nA$ , $A_{ol}=100K$ , Use OP07 for new designs
OP05	SINGLE, ORIGINAL INDUSTRY STANDARD LOW DRIFT OP AMP, See OP07 for new designs
OP07	SINGLE, IND. STD. HIGH ACCURACY OP AMP, $V_{os}=25>75uV$ , $TCV_{os}=.6>1.6uV/C$ , $I_b=2nA$ ,
OP09	QUAD, PINS WITH RM4136, LM148/248.348, HA4741, $V_{os}=750>2000uV$ , $I_b=20>200nA$ , $CMRR=94dB$ , See OP484 for new designs
OP10	DUAL, $V_{os}=.5mV$ , $I_b=2.8nA$ , $CMRR=104dB$ , $V_s=+/3>=-15V$ , See AD708 or OP 270 for new designs
OP11	QUAD, PINS WITH RM4136, LM148/248.348, HA4741, $V_{os}=750>2000uV$ , $I_b=20>200nA$ , $CMRR=94dB$ , See OP 484 for new designs
OP113	SINGLE, $V_{os}=75uV$ , $I_b=600nA$ , $A_{ol}=2V/uV$ , $I_q=1.75mA$ , $V_{out}=V_s-1V$ , $V_{in}=0$ to $V_s-1V$ , $+5.-/-15V$ Rails
OP12	SINGLE, IMPROVED LM108A, $V_{os}=150uV$ to $1mV$ , $TCV_{os}=2.5uV/C$ , $I_b=2nA$ to $5nA$ , $I_q=1mA$ , $I_{out}=5mA$ , See OP77/177 for new designs
OP14	DUAL, IMPROVED LM747, LM1458/1558, $V_{os}=2>5mV$ , $I_b=50$ to $100nA$ , $CMRR=85dB$ , See AD708 or OP270 for new designs
OP15	SINGLE, IMPROVED LOWER POWER VERSION LM155 FAMILY $I_q=5mA$ , $V_{os}=.5>3mV$ , $I_b=50>200pA$ , $BW @A_v=1$ 4MHz, Slew Rate= $10V/u\sec$ , See AD711 or OP42 for new designs
OP16	SINGLE, IMPROVED LOWER POWER VERSION LM156 FAMILY $I_q=5mA$ , $V_{os}=.5>3mV$ , $I_b=50>200pA$ , $BW @A_v=1$ 6MHz, Slew Rate= $18V/u\sec$ , See AD711 or OP42 for new designs
OP162	SINGLE, +3V, +5V or +/-5V, $V_{os}=325uV$ , RAIL TO RAIL OUTPUT, $+2.9V=V_{cm}$ , 15MHz, $11 V/u\sec$ , $9.5nv/\sqrt{Hz}$ , XIND -40>+125C, 700pF CAP LAOD DRIVE
OP17	SINGLE, IMPROVED LOWER POWER VERSION LM157 FAMILY $I_q=5mA$ , $V_{os}=.5>3mV$ , $I_b=50>200pA$ , $BW @A_v=5$ 20MHz, Slew Rate= $45V/u\sec$ , See AD711 or OP42, for new designs

[Click on a part to see Datasheet.](#)

# ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
OP176	SINGLE, AUDIO AMP, NE5534 UPGRADE, BUTLER INPUT STAGE, THD=.0006%, $eN=6nV/\sqrt{Hz}$ , $I_q=5mA$ , SLEWRATE=15V/uSec
OP177	SINGLE, IMPROVED OP-07/OP77 INDUSTRY STD, $V_{os}=10>60\mu V$ , $TCV_{os}=.1$ to $1.2\mu V/C$ , $I_b=1>2.8nA$ ,
OP179	SINGLE, $V_{os}=5mV$ , $I_b=600nA$ , 5 MHz, 3V/sec,+/-5V, Rail to Rail IN/OUT, 80mA OUTPUT,
OP181	SINGLE, $I_q=4\mu A$ , $V_{os}=1mV$ , +2.7V to +/-6V, RAIL to RAIL OUT, w/Offset Adjust
OP183	SINGLE, +3V to $\pm 18$ Supply, $V_{os}=1mV$ , $I_b=600nA$ , 5 MHz, 5V/uSec, 12nV/SQRT HZ, $I_q=1mA/AMP$
OP184	SINGLE,+2V to $\pm 18$ Supply, 4 MHz, 4.5V/uSec, $V_{os}=65\mu V$ , $I_b=300nA$ , $I_q=1mA/AMP$ , 3.9nV/SQRT HZ, RAIL to RAIL IN/OUT
OP186	SINGLE, $I_q=4\mu A$ , $V_{os}=1mV$ , +2.7V to +/-6V, RAIL to RAIL OUT, w/o Offset Adjust
OP191	SINGLE, $I_q=350\mu A/AMP$ , $E_{os}=500\mu V$ , $TCV_{os}=2\mu V/C$ , $I_b=50nA$ , RAIL TO RAIL IN/OUT, +3V to $\pm 6V$ RAIL, 20nV/sqrHz
OP193	SINGLE, $I_q=15\mu A$ , $V_{os}=150\mu V$ , $I_b=20nA$ , $TCV_{os}=2\mu V/C$ , Single or Dual Supply, IMPROVED OP90, Rail to Rail output
OP196	SINGLE, LOW POWER, $I_q=50\mu A$ , +3V to +12V, $I_b=30nA$ , $V_{os}=0.3mV$ , 450KHZ, RAIL to RAIL IN/OUT
OP20	SINGLE, LOW POWER, $I_q=55\mu A$ , SINGLE or DUAL SUPPLY, See OP186 for new designs
OP200	DUAL, VERY LOW DRIFT, $V_{os}=.6\mu V$ , $TCV_{os}=0.5\mu V/C$ , $I_b=2.8nA$ , $A_{ol}=2V/\mu V$ , $I_q=2mA$
OP207	DUAL, VERY LOW DRIFT, $V_{os}=.6\mu V$ , $TCV_{os}=0.5\mu V/C$ , $I_b=2.8nA$ , $A_{ol}=2V/\mu V$ , $I_q=2mA$ , See OP200, AD708 for new designs
OP21	SINGLE, LOW POWER, $I_q=255\mu A$ , SINGLE or DUAL SUPPLY, See OP186 for new designs
OP213	DUAL, $V_{os}=75\mu V$ , $I_b=600nA$ , $A_{ol}=2V/\mu V$ , $I_q=1.75mA$ , $V_{out}=V_s-1V$ , $V_{in}=0$ to $V_s-1V$ , +5./-15V Rails
OP215	DUAL, 14MHz, 10V/uSec, $V_{os}=1>5mV$ , $I_b=100>400pA$ , 2.5uSec to 0.01%, See AD823 for new designs
OP22	SINGLE, $I_q$ is Programmable 0.5uA to 400uA, $V_{os}=.3$ to 1mV, $I_b=5/10nA$ , +3V to +/-15V Rails,
OP220	DUAL, $V_{os}=150>750\mu V$ , $TCV_{os}=1.5\mu V/C$ , $I_b=20nA$ , $I_q=115\mu A$ , +5 or +/-15V Rails
OP221	DUAL, $E_{os}=150>500\mu V$ , $I_b=80>120nA$ , BW=.66MHz, SR=.2V/uSec, $I_q=550\mu A$ , SINGLE or DUAL SUPPLY,
OP227	DUAL, 20MHz, 1.3V/uSec, $V_{os}=80>180\mu V$ , $TCV_{os}=1\mu V/C$ , $I_b=80nA$ , $E_{noise}=.18\mu V$ pp
OP249	DUAL, 3.5MHz, 18V/uSec, $V_{os}=.5mV$ , $TCV_{os}=5\mu V/C$ , $I_b=75pA$ , JFET LOW OFFSET

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
OP250	DUAL, CMOS, LOW POWER 600 uA/AMP, RAIL TO RAIL IN/OUT, +3V to +6V RAIL to RAIL IN/OUT, 6 MHz, 5V/usec, Ib=60pA
OP260	DUAL, 550V/usec, 40MHz
OP262	DUAL, +3V, +5V or +/-5V, Vos=325uV, RAIL TO RAIL OUTPUT, +2.9V=Vcm, 15MHz, 11 V/usec, 9.5nv/sqrt hz, XIND - 40>+125C, 700pF CAP LOAD DRIVE
OP27	SINGLE, 20MHz, 1.3V/usec, Ib=80nA, Enoise=0.18uV pp
OP270	DUAL, 20MHz, 1.3V/usec, Vos=75>250uV, TCvos=1uV/C, Ib=2>60nA, SR=1.7V/usec, Enoise=.18uV pp
OP271	DUAL, 20MHz, 1.3V/usec, Vos=200>400uV, TCvos=2>5uV/C, Ib=20>60nA, SR=5.5V/usec, Enoise=.18uV pp
OP275	DUAL, AUDIO AMP, NE5532 UPGRADE, BUTLER INPUT STAGE, THD=.0006%, eN=6nv/sqrtHz, Iq=5mA, SR=15V/usec
OP279	DUAL, Vos=5mV, Ib=600nA, 5 MHz, 3V/sec,+/-5V, Rail to Rail IN/OUT, 80mA OUTPUT,
OP281	DUAL, Iq=8uA, Vos=1mV, +2.7V to +/-6V, RAIL to RAIL OUT
OP282	DUAL, 7V/usec, 4MHz, Ib=100pA, Iq=250uA, Vcm=+Vs
OP283	DUAL OP183, +3V to ±18 Supply, 5 MHz, 5V/usec, 12nV/SQRT HZ, Iq= 1mA/AMP
OP284	DUAL,+2V to ±18 Supply, 4 MHz, 4.5V/usec, Vos=65uV, Iq= 1mA/AMP, 3.9nV/SQRT HZ, RAIL to RAIL IN/OUT
OP285	DUAL, AUDIO AMP, NE5532 UPGRADE, BUTLER INPUT STAGE, LOW OFFSET
OP290	DUAL, ULTRA LOW POWER, Vos=150 to 450uV, Ib=15>25nA, Iq=15uA, Single +3V to +/-15V Rails, See OP293/296
OP291	DUAL, Iq= 350 uA/AMP, Eos=500uV, TCvos=2uV/C, Ib=50nA, RAIL TO RAIL IN/OUT, +3V to ±6V RAIL, 20nV/sqtrhz
OP292	DUAL, 4 MHz, 3V/usec, +4.5 to ±15V, 20nV/SQRT HZ, 1mA/AMP, ZERO IN ZERO OUT
OP293	DUAL, Iq=15uA, Vos=150uV, Ib=20nA, TCvos=2uV/C, Single or Dual Supply, IMPROVED OP290, Rail to Rail output
OP295	DUAL, BW=75KHZ, Vos=500uV, Ib=20nA, Iq=150 uA/AMP, RAIL TO RAIL OUTPUT
OP296	DUAL, LOW POWER, Iq=50uA, +3V to +12V, Ib=30nA, Vos=0.3mV, 450KHZ, RAIL to RAIL IN/OUT
OP297	DUAL, Vos=25uV,TCvos=0.6uV/C, Ib=.15nA, Aol=0.2V/uV, Iq=0.6mA
OP32	SINGLE, Iq PROGRAMMABLE .from 5uA to 2mA, Single or Dual Supplies
OP37	SINGLE, BW @Av=5 45MHz, 11V/usec, Ib=80nA, Enoise=.18uV pp

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
OP400	QUAD, Vos=.175uV, Ib=650nA, Aol=2V/uV, Iq=3.5mA, SINGLE SUPPLY
OP41	SINGLE, Ib=5pA, see AD645A for new designs
OP413	QUAD, Vos=75uV, Ib=600nA, Aol=2V/uV, Iq=1.75mA, Vout=Vs-1V, Vin=0 to Vs-1V, +5./-15V Rails
OP42	SINGLE, 10MHz, 40V/usec, Vos=.75 to 5mV, Ib=200 to 250pA, CMRR=80>88 dB, Tsettling=1.2usec to 0.01%
OP420	QUAD, LOW POWER, Iq=200uA, Eos=6mV, TCVos=25uV/C, Single or Dual SUPPLY
OP421	QUAD, Vos=2.5>6mV, Ib=5>20nA, BW==1MHz, SINGLE or DUAL SUPPLY, Iq=1mA
OP450	QUAD, CMOS, LOW POWER 600 uA/AMP, RAIL TO RAIL IN/OUT, +3V to +6V RAIL to RAIL IN/OUT, 6 MHz, 5V/usec, Ib=60pA
OP462	QUAD, +3V, +5V or +/-5V, Vos=325uV, RAIL TO RAIL OUTPUT, +2.9V=Vcm, 15MHz, 11 V/usec, 9.5nv/sqrt hz, XIND - 40>+125C, 700pF CAP LOAD DRIVE
OP467	QUAD, BW= 30MHz, 170V/usec, Settling Time=200nsec to .01%, Vos=500uV, Ib=600nA
OP470	QUAD, 20MHz, 1.3V/usec, Ib=80nA, Enoise=.18uV pp
OP471	QUAD, 20MHz, 5.5V/usec, Ib=80nA, Enoise=.18uV pp, LOW COST
OP481	QUAD, Iq=5uA, Vos=700uV, +2.7V to +/-6V, RAIL to RAIL OUT
OP482	QUAD, 7V/usec, 4MHz, Ib=100pA, Iq=250uA, Vcm=+Vs
OP484	QUAD, +2V to ±18 Supply, 4 MHz, 4.5V/usec, Vos=65uV, Iq= 1mA/AMP, 3.9nV/SQRT HZ, RAIL to RAIL IN/OUT, Recommended for New Designs
OP490	QUAD, ULTRA LOW POWER, Vos=150 to 450uV, Ib=15>25nA, Iq=15uA, Single +3V to +/-15V Rails, See OP493/496 for new designs
OP491	QUAD, Iq= 350 uA/AMP, Eos=500uV, TCVos=2uV/C, Ib=50nA, RAIL TO RAIL IN/OUT, +3V to ±6V RAIL, 20nV/sqtrhz
OP492	QUAD, 4 MHz, 3V/usec, +4.5 to ±15V, 20nV/SQRT HZ, 1mA/AMP, ZERO IN ZERO OUT
OP493	QUAD, Iq=15uA, Vos=150uV, Ib=20nA, TCVos=2uV/C, Single or Dual Supply, IMPROVED OP490, Rail to Rail output
OP495	QUAD, BW=75KHZ, Vos=500uV, Ib=20nA, Iq=150 uA/AMP, RAIL TO RAIL OUTPUT
OP496	QUAD, LOW POWER, Iq=50uA, +3V to +12V, Ib=30nA, Vos=0.3mV, 450KHZ, RAIL to RAIL IN/OUT
OP497	QUAD, Vos=25uV,TCVos=0.6uV/C, Ib=.15nA, Aol=0.2V/uV, Iq=0.6mA

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
OP64	SINGLE, HIGH SPEED, DECOMP'D, $A_v=5$ , 130V/uV, See AD840 for new designs
OP77	SINGLE, IMPROVED OP07, VERY LOW DRIFT, $V_{os}=.6\mu V$ , $I_b=2.8nA$ , $A_{ol}=2V/\mu V$ , $I_q=2mA$
OP80	SINGLE, CMOS OP AMP, $V_{os}=1.5mV$ , CMRR=50dB, $I_b=.250fA$ to 1pA, +5V Rail, See OP8531 for new designs
OP90	SINGLE, ULTRA LOW POWER, $V_{os}=150$ to 450uV, $I_b=15>25nA$ , $I_q=15\mu A$ , Single +3V to +/-15V Rails, See OP186/193/196
OP97	SINGLE, LOW POWER, $V_{os}=25\mu V$ , $I_b=.15nA$ , $A_{ol}=0.2V/\mu V$ , $I_q=0.6mA$
PKD01	PEAK DETECTOR
PM1012	SINGLE, LOW POWER OP07, See OP97 or AD705 for new designs
PM111	SINGLE, 180nsec, $V_{os}=3mV$ , $I_b=5nA$ , +/-15V Rails, FULL MILTEMP RANGE
PM139	QUAD, 1300nsec, $V_{os}=5mV$ , $I_b=100nA$ , +5V Rail, FULL MIL RANGE ONLY, See AD8564 for new designs
PM139A	QUAD, 1300nsec, $V_{os}=2mV$ , $I_b=100nA$ , +5V Rail, Full Mil temp range version only, See AD8564 for new signs
PM7628	DUAL, 8 BIT, INVERTED R2R, CURRENT OUTPUT, TTL @, +5V or +15V SUPPLY
PM7645	PM7545, 12 BIT I/O, TTL and CMOS COMPATIBLE for $V_{dd}=+15V$ .
REF01	+10V, $V_{os}=3/5mV$ , $TCV_o=8.5$ to 65ppm, Line Regulation=.01%/V, Load Regulation=.01%/mA, $I_q=1.4mA$ , Band Gap
REF02	+5V, $V_{os}=+/-15/25mV$ , $TCV_o=8.5$ to 250ppm, Line Regulation=.01%/V, Load Regulation=.01%/ma, $I_q=1.4mA$ , w/Temp Output=2.3mV/C, Band Gap
REF03	+2.5V, $V_{os}=+/-15mV$ , $I_q=1.4mA$ , $TCV_o=50ppm$ , Line Regulation=50ppm/V, Load Regulation=100ppm/mA
REF05	+5V, $V_{out}=+5V$ +/-15mV, Line/Load Regulation=.01%/V/01%/mA, $TCV_{out}=8.5>25ppm$ , MILITARY GRADE ONLY
REF08	-10V or -10.24V, $TC=50$ to 100 ppm
REF10	+10V +/-30mV, $TC=8.5$ to 25 ppm, LONG TERM DRIFT SPEC=50ppm/1000HRS, MILITARY ONLY
REF191	+2.048V, $I_q=45\mu A$ , Initial Accuracy=2/5/10mV, $I_{out}=30mA$ , Band gap
REF192	+2.5V, $I_q=45\mu A$ , Initial Accuracy=2/5/10mV, $I_{out}=30mA$ , Band gap
REF193	+3.0V, $I_q=45\mu A$ , Initial Accuracy=2/5/10mV, $I_{out}=30mA$ , Band gap
REF194	+4.5V, $I_q=45\mu A$ , Initial Accuracy=2/5/10mV, $I_{out}=30mA$ , Band gap

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
REF195	+5.0V, Iq=45uA, Initial Accuracy=2/5/10mV, Iout=30mA, Band gap
REF196	+3.3V, Iq=45uA, Initial Accuracy=2/5/10mV, Iout=30mA, Band gap
REF198	+4.096V, Iq=45uA, Initial Accuracy=2/5/10mV, Iout=30mA, Band gap
REF43	+2.5V, Vos=+/-15/25mV, TCVo=6 to 10ppm, Line Regulation=2ppm/V, Load Regulation=20ppm/ma, Iq=0.45mA, w/Temp Output=1.9mV/C, Band Gap
RPT82	PCM REPEATERS, 100KHZ to 3MHz,
RPT83	PCM REPEATERS, 100KHZ to 3MHz, with CLOCK SHUTDOWN
SMP04	QUAD, with int. hold caps, Drop Rate=.25mV/msec, Acq Time=7usec, +12 or +/-5V Rails, +/-3V INPUT RANGE
SMP08	8 Channel OUT, 1 Channel IN, Tacq=7usec, Drop Rate=20mV/sec, 0.01% Linearity, +/-3V INPUT RANGE RANGE
SMP11	SINGLE, 1MHz, 3 usec to 0.01%, SMP10 LOSER DC SPECS
SMP18	8 Channel OUT, 1 Channel IN, Tacq=3.5usec, Drop Rate=20mV/sec, 0.01% Linearity, +/-3V INPUT RANGE
SSM2000	HUSH STEREO NOISE REDUCTION (-25dB ANY AUDIO SOURCE) with ADAPTIVE THRESHOLD
SSM2005	CIRCLE SURROUND, 5 Channel SOUNDFIELD GENERATOR FROM LEFT/RIGHT UNENCODED STEREO SOURCES, +7 to 20V RAIL
SSM2017	SINGLE, LOW NOISE, AUDIO PRE-AMP, eN=.95uVpp, SNR=-121dBu, THD+N=0.001%, Gain=1>1000
SSM2018	SINGLE, AUDIO VCA/OVCE, 118 dB, dynamic range, buffered outputs on G and 1-G, See SSM2018T
SSM2018T	SINGLE, AUDIO VCA/OVCE, 117 dB, dynamic range,TRIMMED, G & G-1 are VOLTAGE OUTPUTS, TRIMMED FOR LOW DISTORTION
SSM2024	QUAD VCA, see SSM2164
SSM2118T	SINGLE, AUDIO VCA/OVCE, 117 dB, dynamic range, G OUTPUT=DIFF CURRENT OUT, 1-G OUTPUT is VOLTAGE OUTPUT, TRIMMED FOR LOW DISTORTION
SSM2120	DUAL VCA, WITH TWO 100dB LEVEL DETECTORS, .01% THD@ +10dBV In/Out, 100ddB Dynamic Range
SSM2122	DUAL VCA, .01% THD @ +10dBV In/Out, 100dB Dynamic Range
SSM2125	DOLBY PRO LOGIC SURROUND MATRIX DECODER, 35 dB , center to left/right
SSM2126	DOLBY PRO LOGIC SURROUND MATRIX DECODER, 25 dB , center to left/right

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## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
SSM2135	DUAL, Vos=.175uV, Ib=650nA, Aol=2V/uV, Iq=1.75mA, SINGLE SUPPLY
SSM2141	AUDIO DIFFERENTIAL LINE RECEIVER, Av=1, THD+N=.001%, SNR=100dBu, CMRR=80dB
SSM2142	AUDIO, BALANCED DIFFERENTIAL LINE DRIVER, THD+N=.006%, SNR=93.4dBu,
SSM2143	AUDIO DIFFERENTIAL LINE RECEIVER, Av=1/2, THD+N=.001%, SNR=100dBu, CMRR=80dB
SSM2160	HEX VOLUME CONTROL, CLICKLESS,THD+N=0.01% at 0dBu, Noise Floor=90dBu (24.5uV) RTI, CH GAIN 0 to +31dB (5 BIT D/A 32 STEPS), MASTER CH 0 to -131dB (7 BIT D/A 128 STEPS), BUFFERED OUTPUT
SSM2161	QUAD VOLUME CONTROL, CLICKLESS,THD+N=0.01% at 0dBu, Noise Floor=90dBu (24.5uV) RTI, CH GAIN 0 to +31dB (5 BIT D/A 32 STEPS), MASTER CH 0 to -131dB (7 BIT D/A 128 STEPS), BUFFERED OUTPUT
SSM2163	8 Channel AUDIO MIXER, MIX 8 CH. DOWN TO STEREO PAIR, VOLUME ADJUST ANY INPUT, THD=.007%, ATTEN=63dB 1dB STEPS, NOISE -82dBu, +5 or +/-5V
SSM2164	QUAD VCA, 20dB GAIN RANGE, 0.02% DISTORTION, CLASS A or AB, CURRENT IN/OUT
SSM2165	MICROPHONE PREAMP, with VARIABLE COMPRESSION 1:1 to 20:1& PRESET NOISE GATING, VCA GAIN 0 to 20dB, UNITY GAIN INPUT BUFFER
SSM2166	MICROPHONE PREAMP, with VARIABLE COMPRESSION 1:1 to 20:1& PRESET NOISE GATING, VCA GAIN 0 to 20dB, 0 to 20dB GAIN INPUT BUFFER
SSM2210	DUAL, NPN, Vos=200uV, TC Vos=2uV/c, Hfe=300, enoise=1nV/SQRTHz
SSM2211	SINGLE, 1 WATT DIFF OUTPUT POWER AMP, +2.7V>+5.5V, RAIL to RAIL IN/OUT, 4MHz, 4V/usec, Ib=300pA, THD @ 500mW=.45%, 1% @ 1WATT, Iq=10.5mA, Isdown=1nA
SSM2220	DUAL, PNP, Vo=200uV, TC Vos=2uV/c, Hfe=70, enoise=1nV/SQRTHz
SSM2275	DUAL, 8.7 MHZ, 15V/usec, en=7nV/SQRTHZ, THD+N=0.004%,M RAIL TO RAIL OUTPUT
SSM2402	DUAL, 85 ohm, SPST, Ton=10msec, Toff=4msec, AUDIO CLICKLESS
SSM2404	QUAD, 28 ohm, SPST, Ton=NS, Toff=30msec, AUDIO CLICKLESS
SSM2412	DUAL, 85 ohm, SPST, Ton=3.5msec, Toff=1.5msec, AUDIO CLICKLESS
SSM2475	QUAD, 8.7 MHZ, 15V/usec, en=7nV/SQRTHZ, THD+N=0.004%,M RAIL TO RAIL OUTPUT
SW06	QUAD, 80 ohm, SPST, DUAL OPEN, DUAL CLOSED, Ron=80ohms, ton.toff=.5/.4usec, Pin Compatible LM11333/13333, overvoltage protected, +/- 20V
SW201	QUAD, SPST, 150 ohm, LOGIC 1=SWITCH OFF, overvoltage protected, +/- 20V

[Click on a part to see Datasheet.](#)

## ANALOG DEVICES CD REFERENCE

PART	DESCRIPTION
SW202	QUAD, SPST, 150 ohm, LOGIC 1=SWITCH ON, overvoltage protected, +/- 20V
TMP01	TEMP SENSOR/PROGRAMMABLE CONTROLLER, WITH WINDOW COMPARATOR, w/VPAT OUTPUT=1.5V +/- 5mV/C, 1 DEGREE ACCURACY, +5V Rail
TMP03	TEMPERATURE SENSOR, DUTY CYCLE MODULATED OUTPUT @ 35HZ, Temp Range=40>+100C, +5V @ 1mA, OPEN COLLECTOR OUTPUT
TMP04	TEMPERATURE SENSOR, DUTY CYCLE MODULATED OUTPUT @ 35HZ, -40>+100C, +5V @ 1mA, TTL/CMOS OUTPUT
TMP12	AIRFLOW AND TEMPERATURE SENSOR, 100ohm heater for emulating power IC or PENTIUM CPU, programmable hysteresis, DUAL SETPOINTS, -40>+125C, +4.5>13.2V, Iq=600uA
TMP17	TEMPERATURE SENSOR, CURRENT OUTPUT I=1uA/DEGREE KELVIN, -40 to +105C, AD590 in 8 LEAD SOIC
TMP35	LINEAR OUTPUT -40 +150C, 10mV/C, +/-2 or 3C ACCURACY, Vout=250mV +/-10mV/C, Iq=50uA, V=+2.7>+5.5V, Iq=5uA in shutdown
TMP36	LINEAR OUTPUT -40 +150C, +/-2 or 3C ACCURACY, Vout=750mV +/-10mV/C, Iq=50uA, V=+2.7>+5.5V, Iq=5uA in shutdown
TMP37	LINEAR OUTPUT -40 +150C, +/-2 or 3C ACCURACY, Vout 500mV +/-20mV/C, Iq=50uA, V=+2.7>+5.5V, Iq=5uA in shutdown

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